



FIG 2

[illegible]

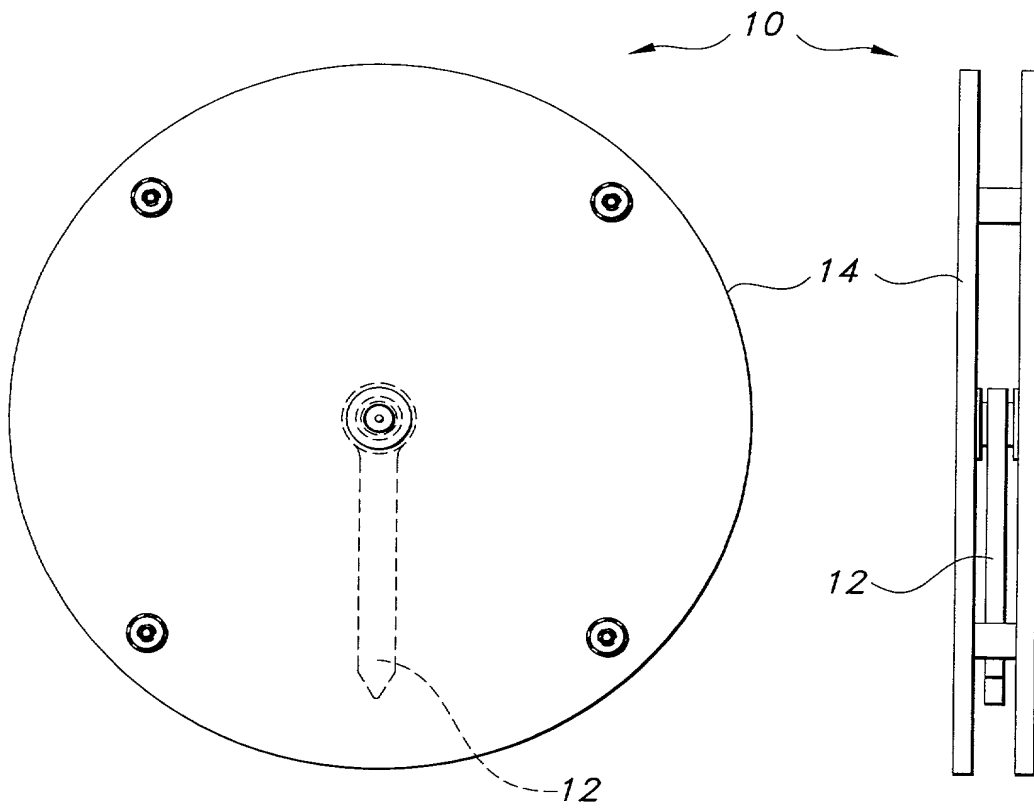


FIG 3A

FIG 3B

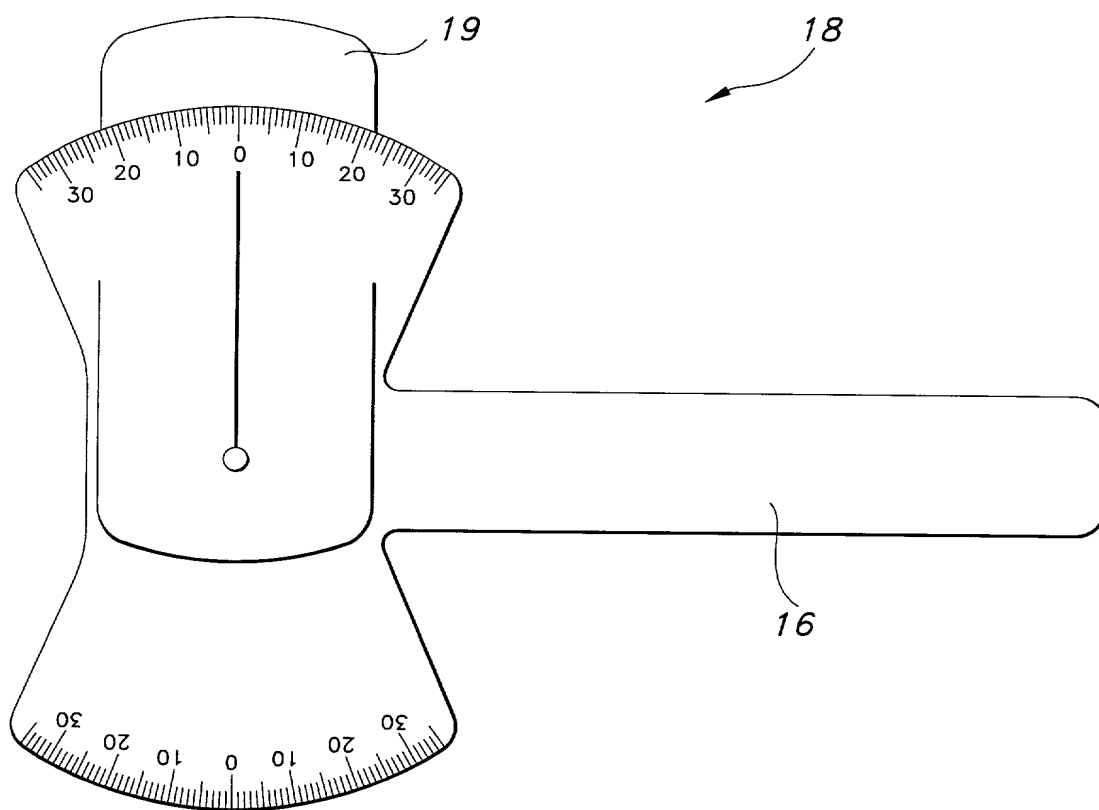


FIG 4



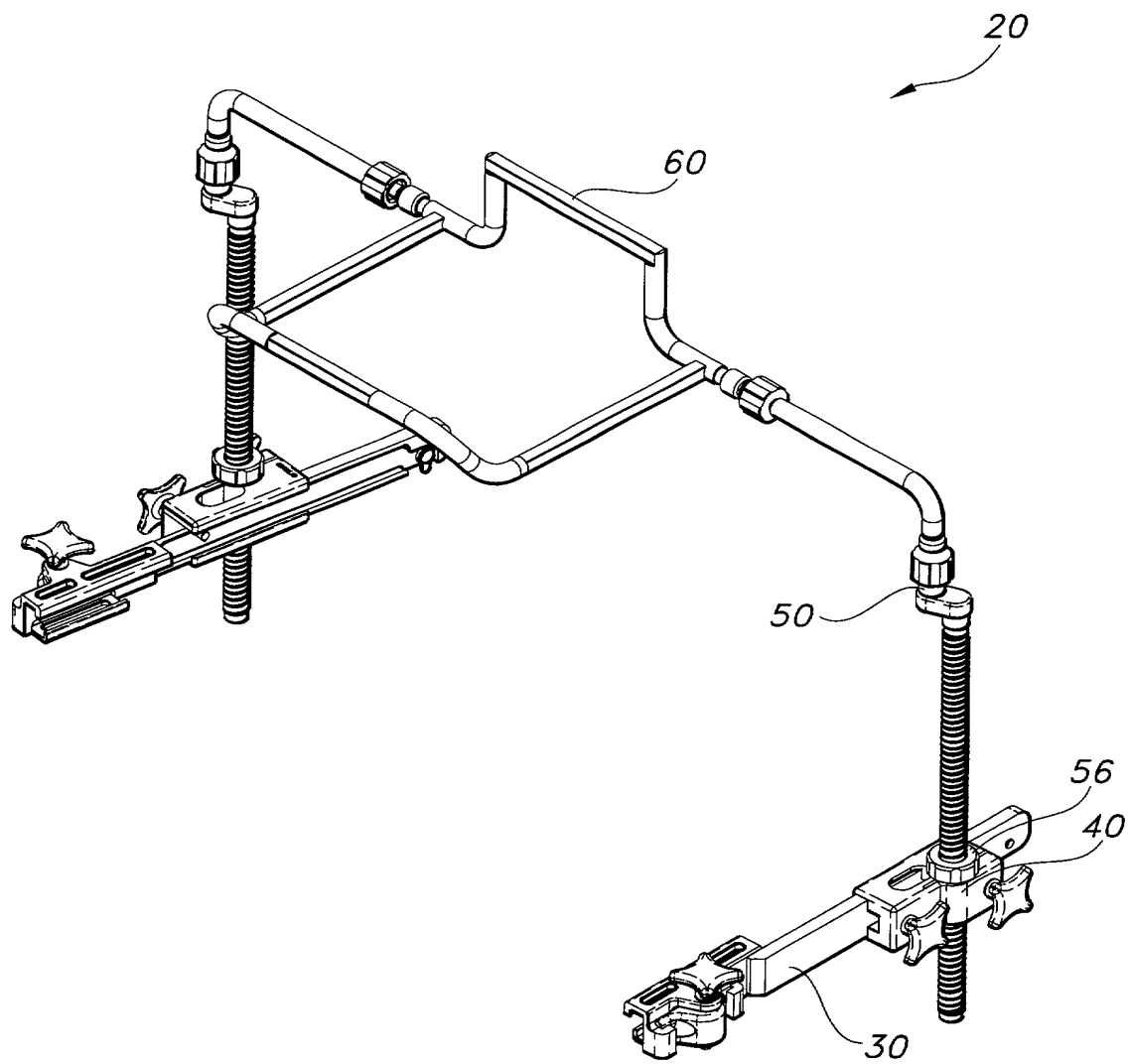
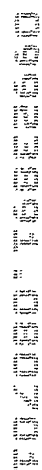


FIG 5



+

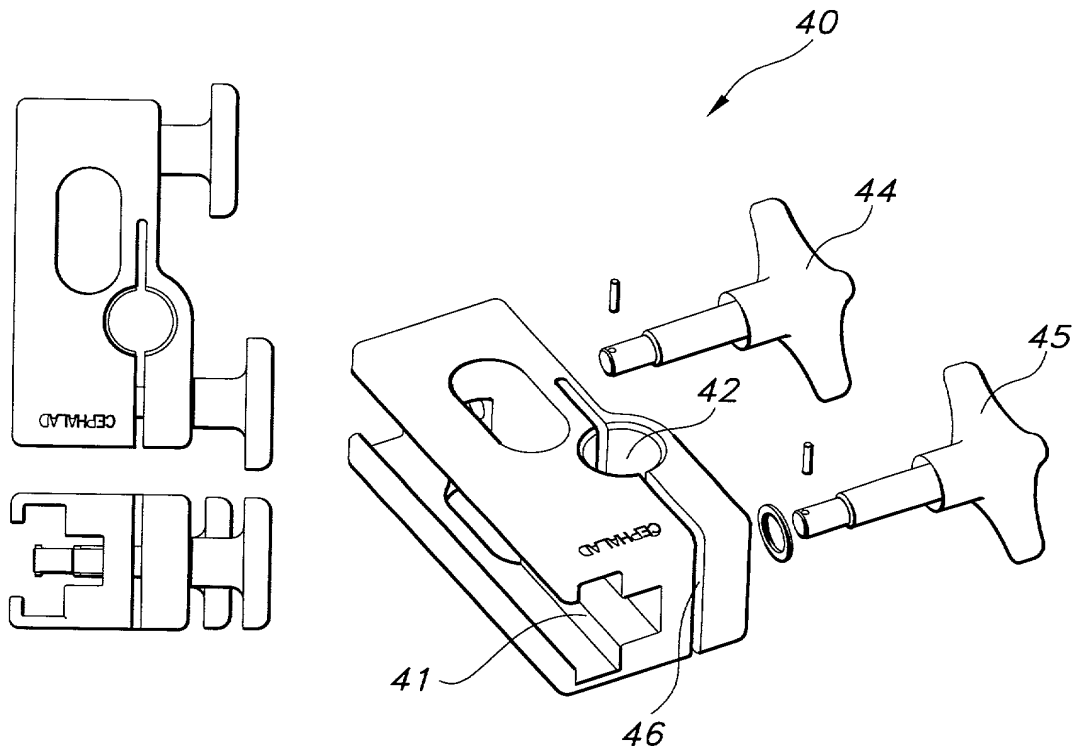


FIG 7

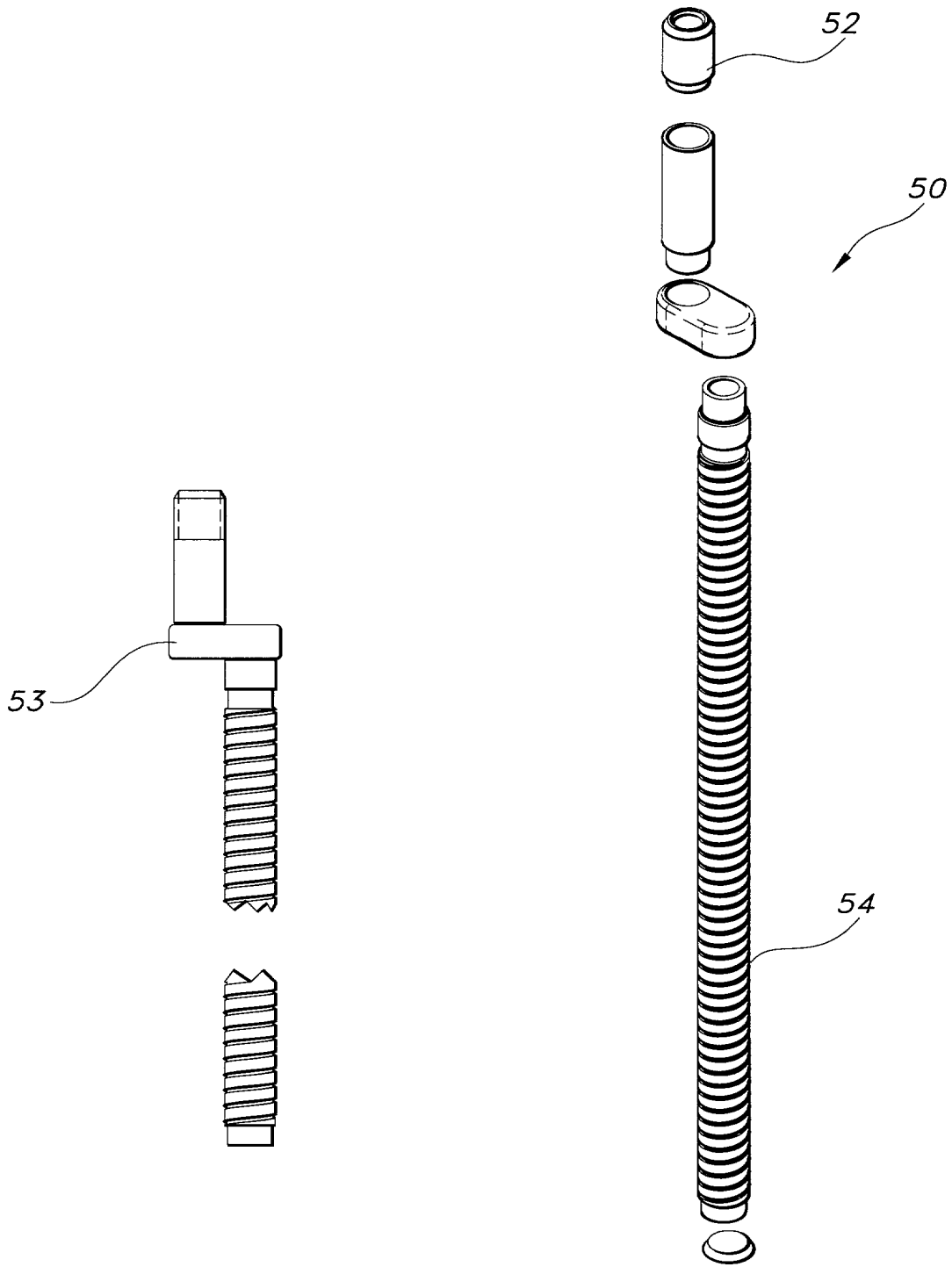


FIG 8

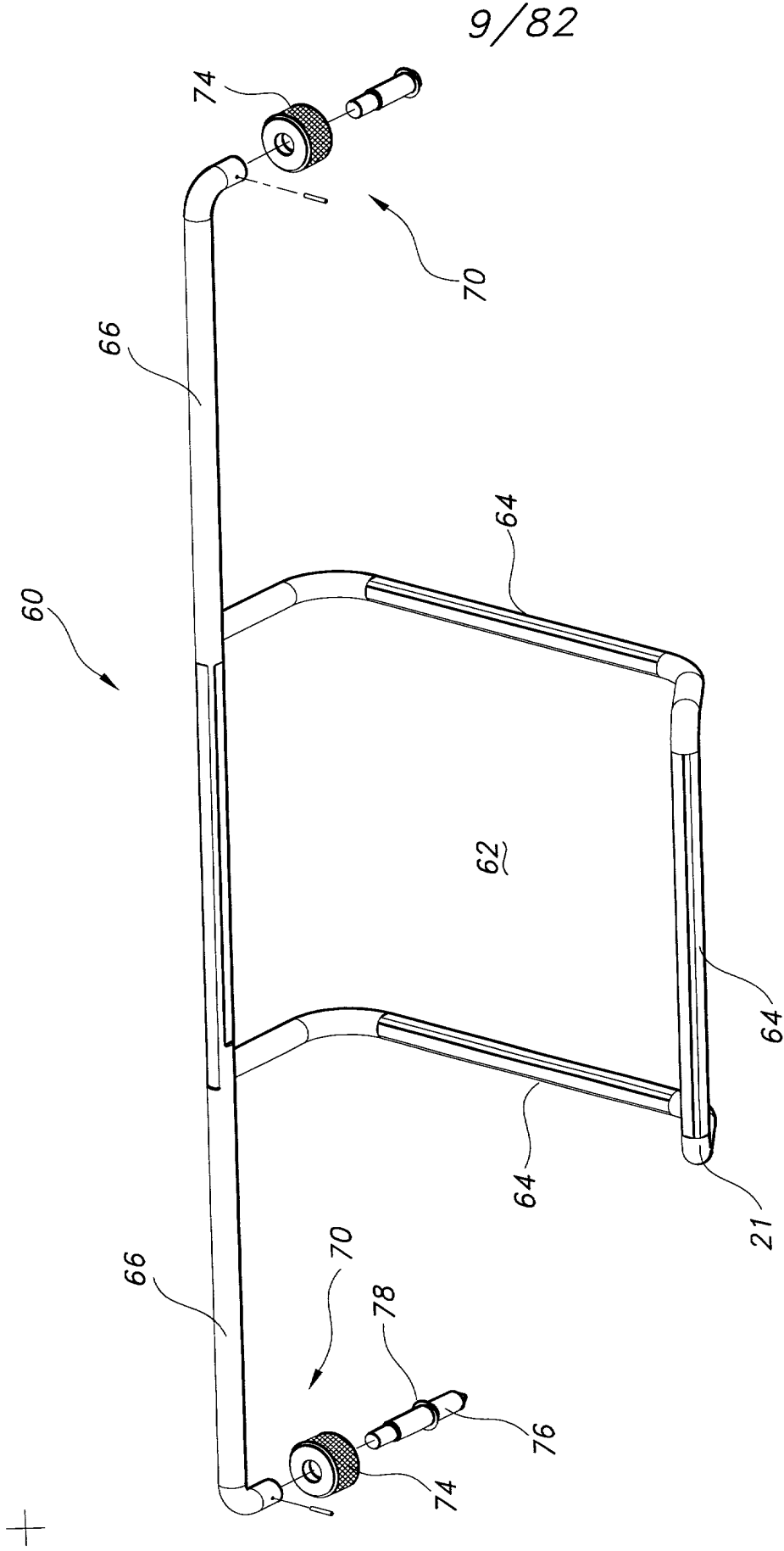


FIG 9

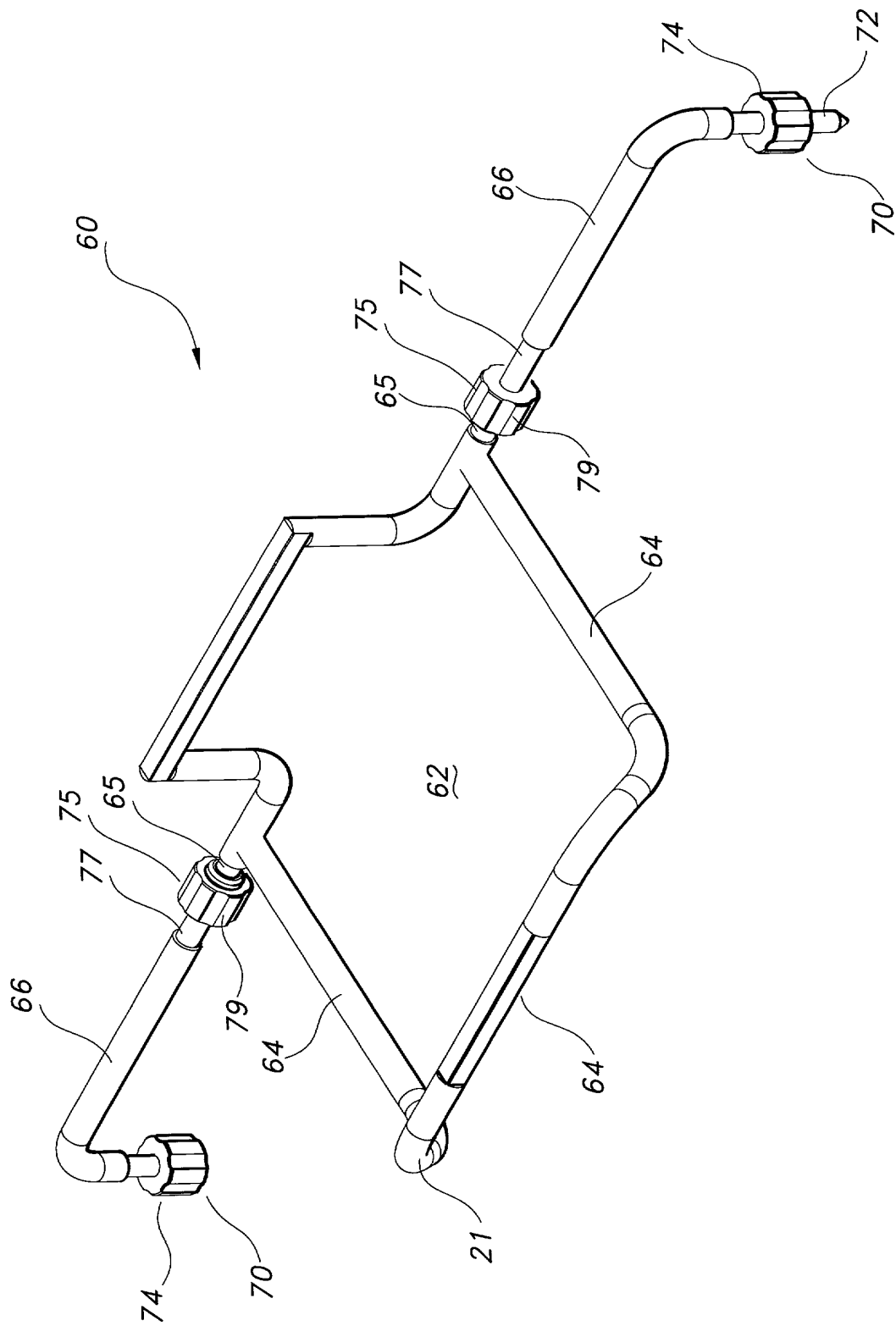


FIG 10

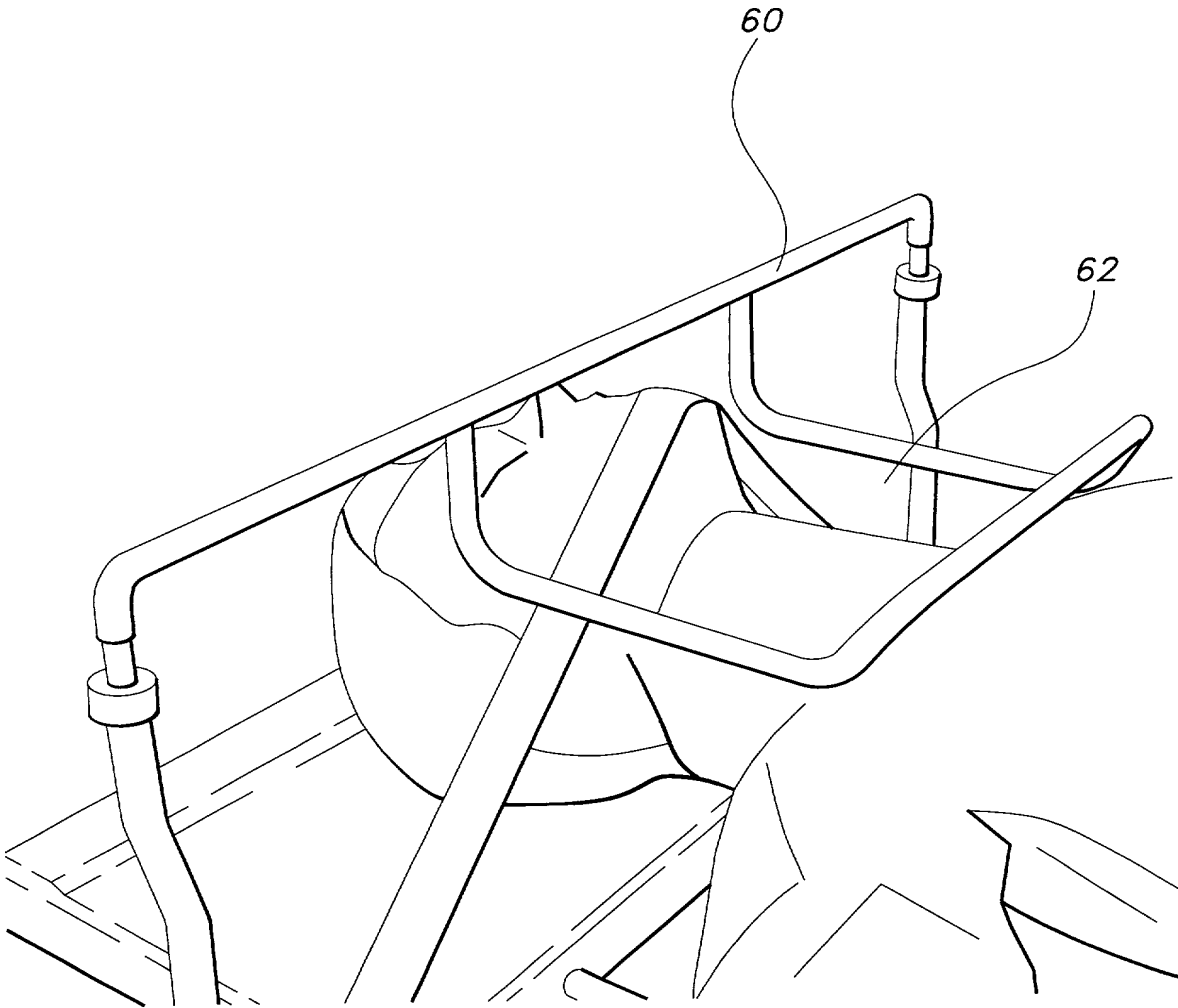


FIG 11



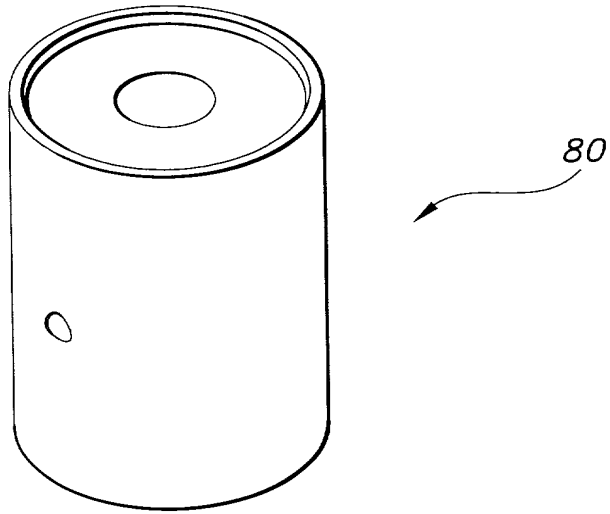


FIG 12



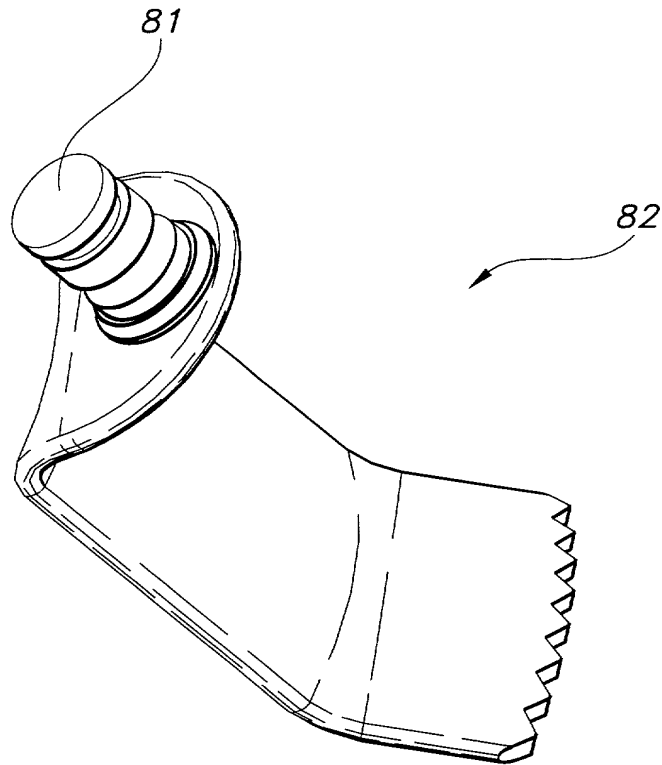


FIG 13

14/82

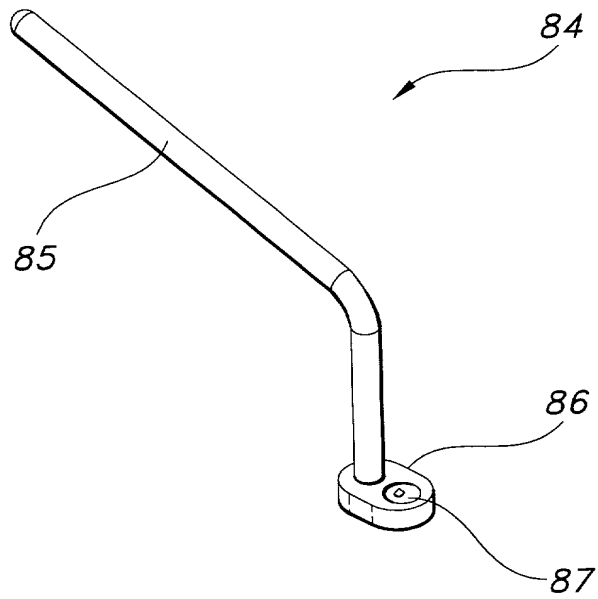


FIG 14A

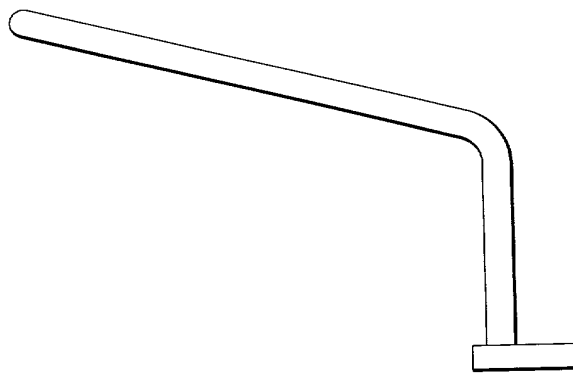


FIG 14B

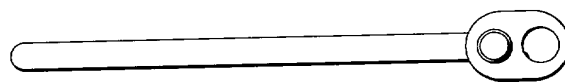


FIG 14C

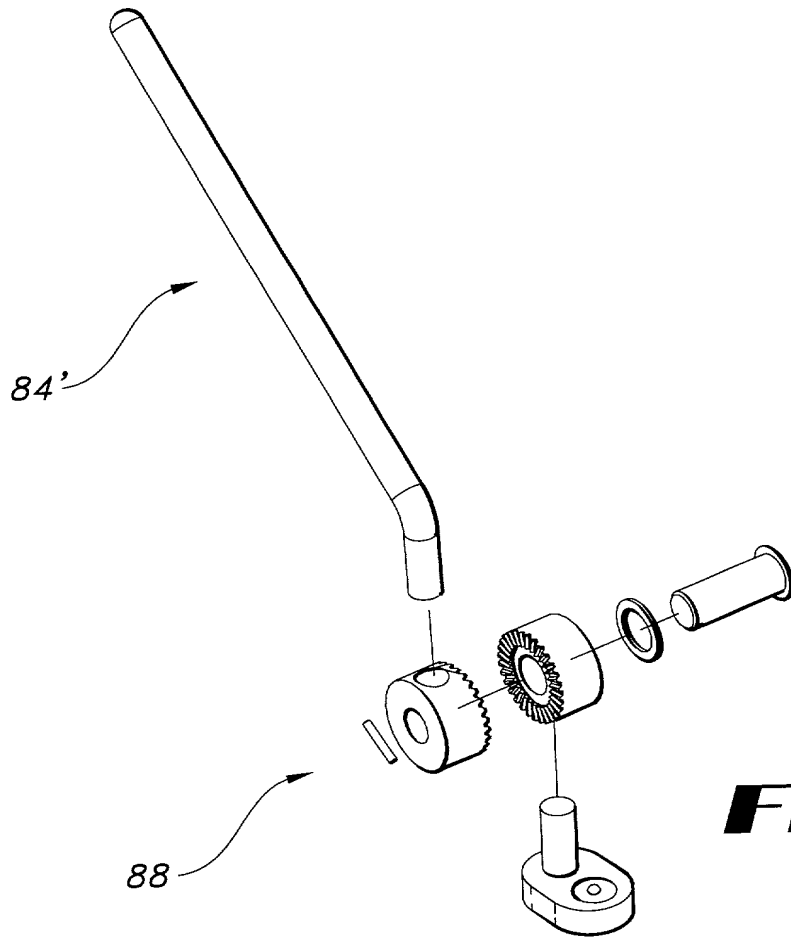


FIG 14E

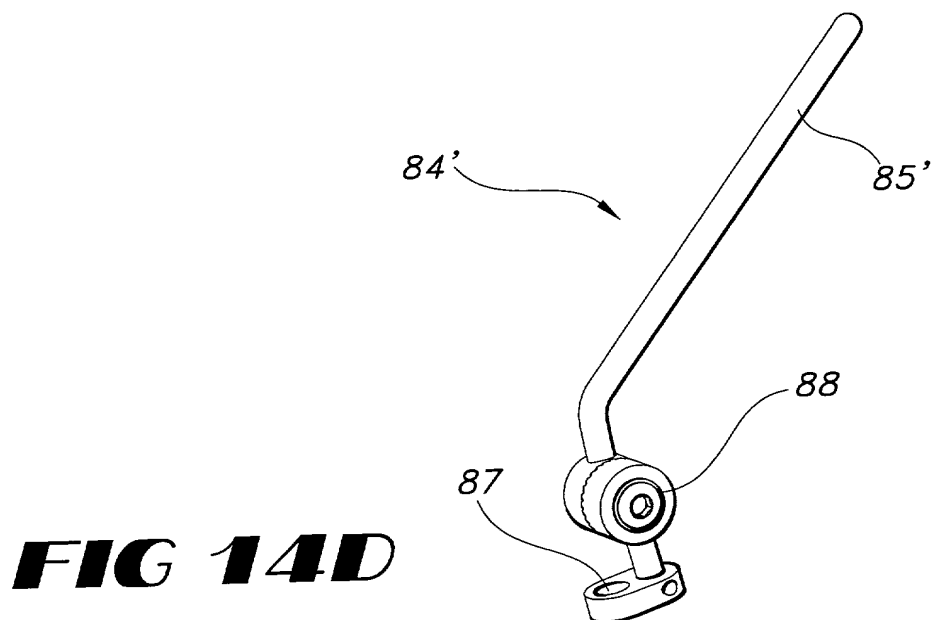


FIG 14D

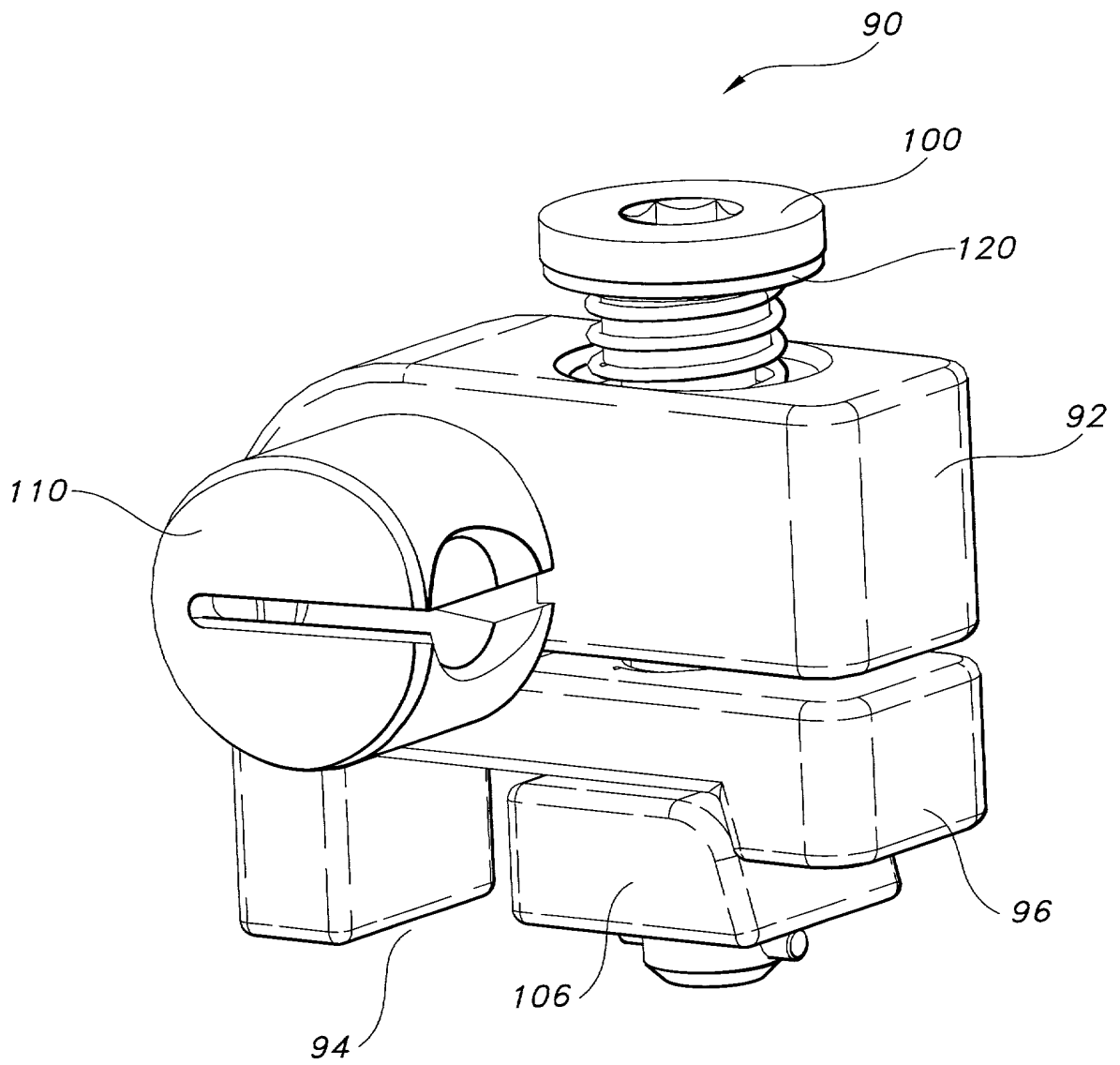


FIG 15

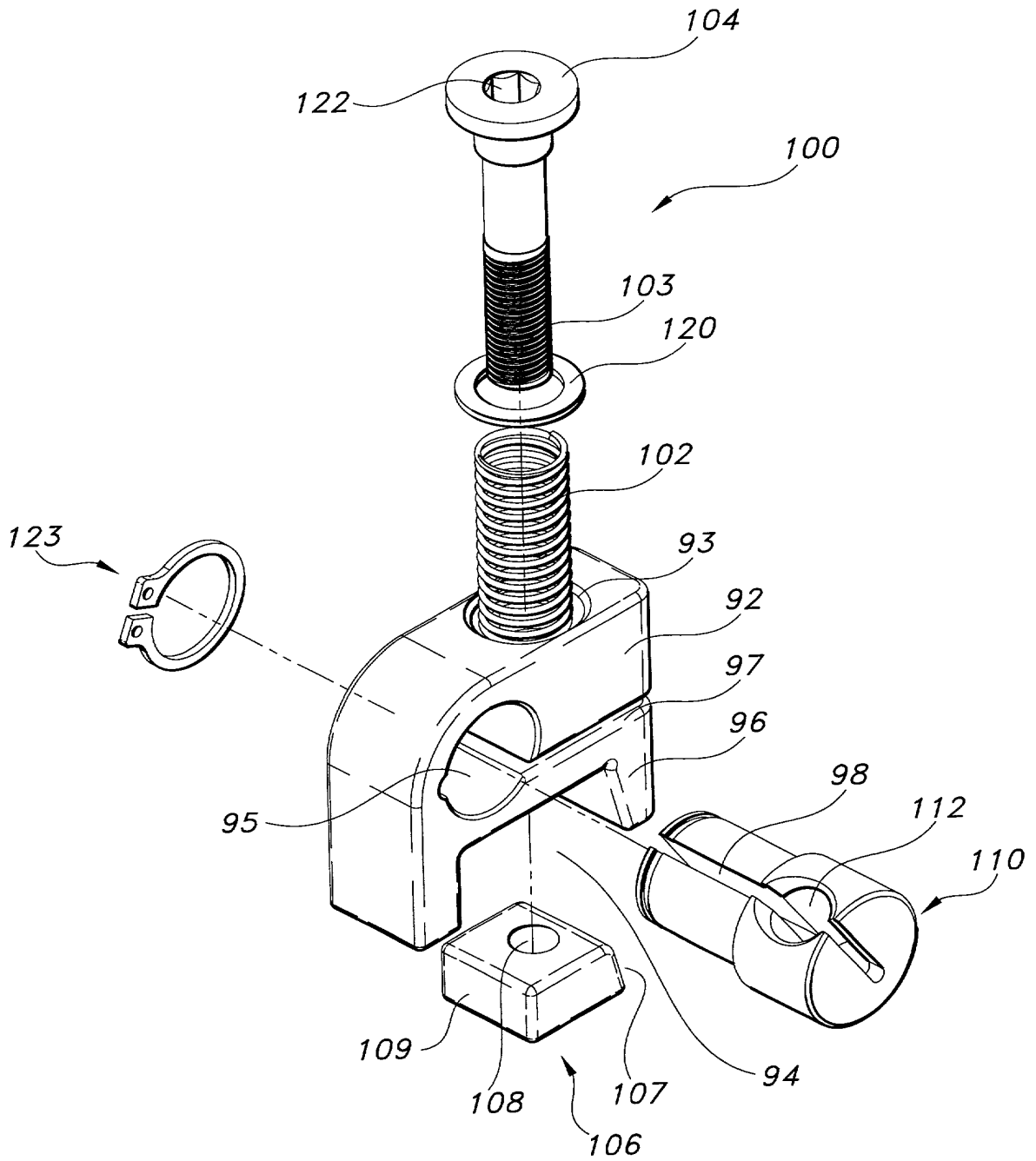


FIG 16

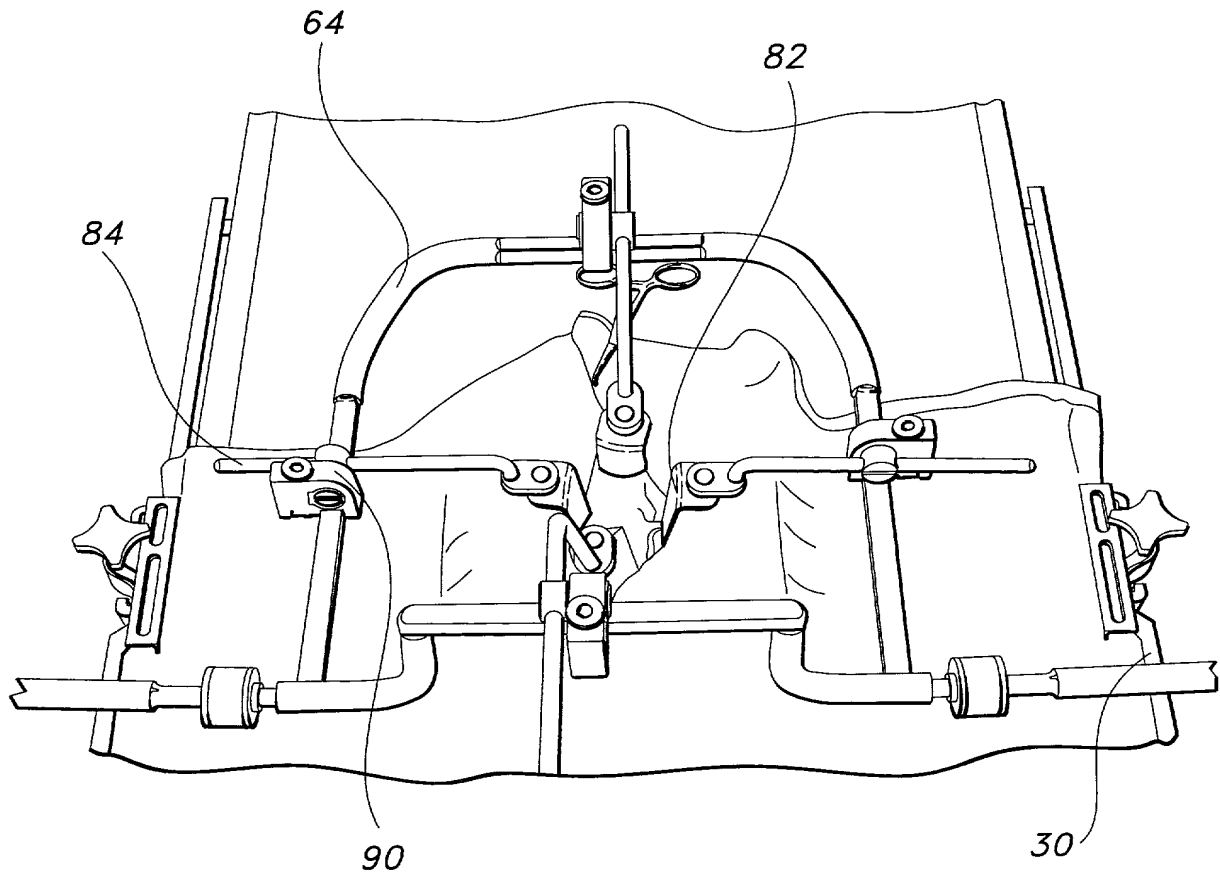


FIG 17

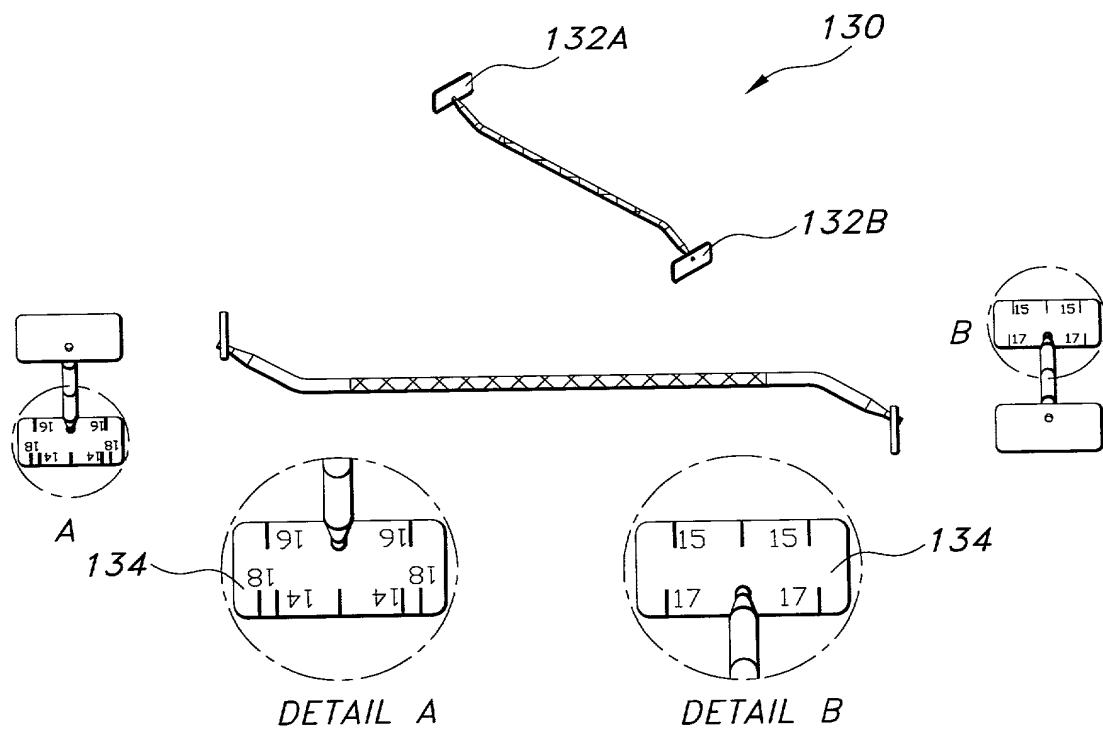


FIG 18

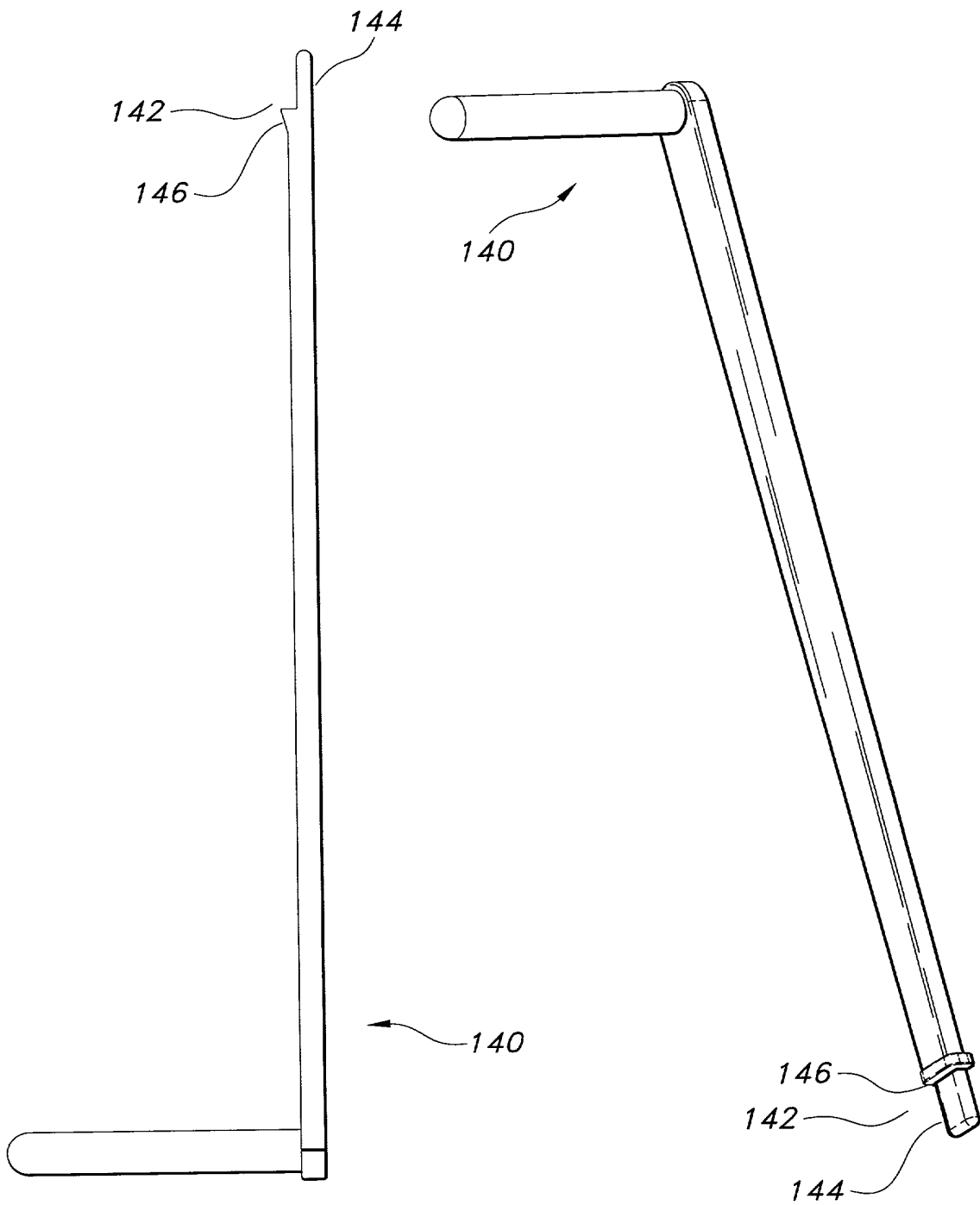


FIG 19

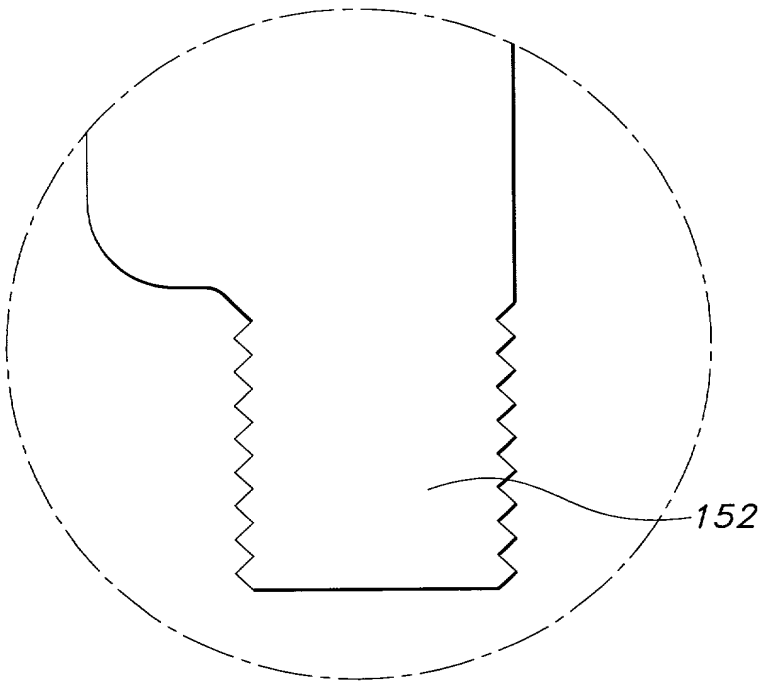


FIG 20B

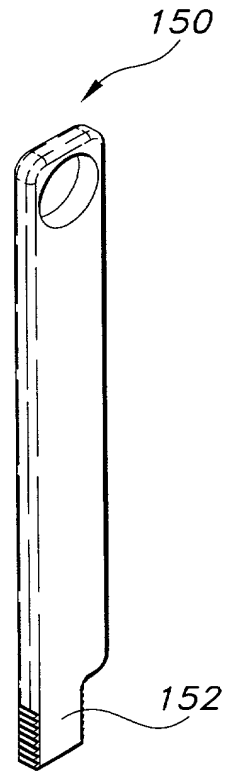


FIG 20A

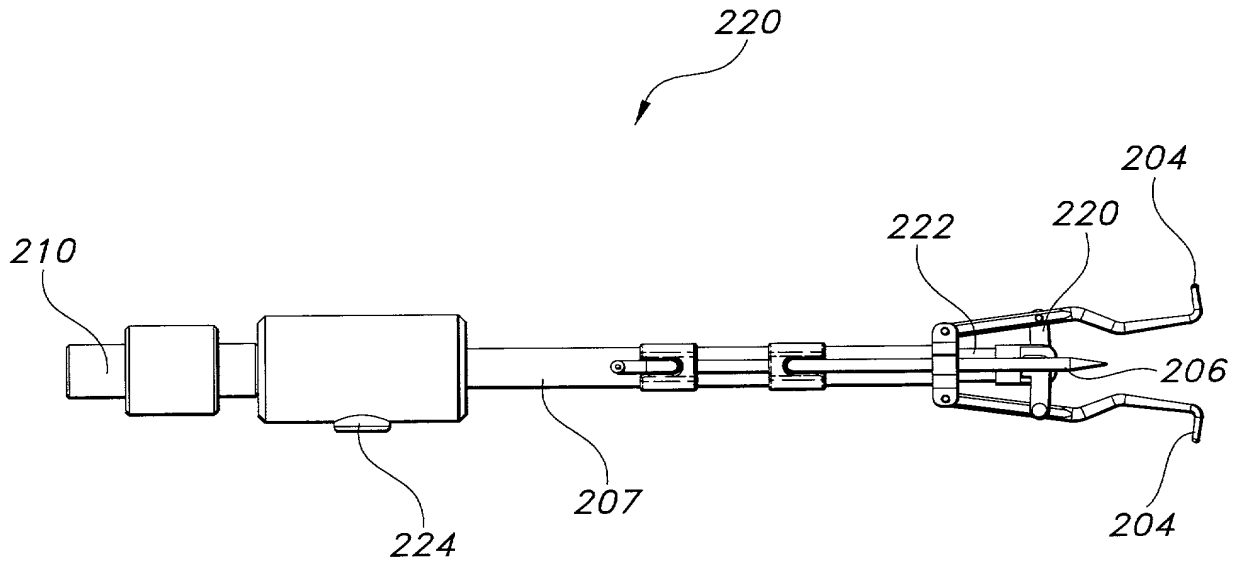


FIG 21

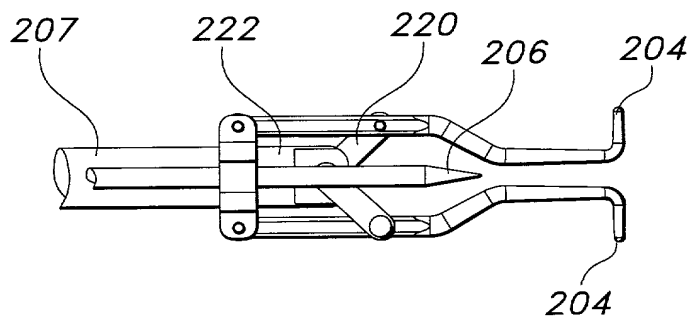


FIG 22

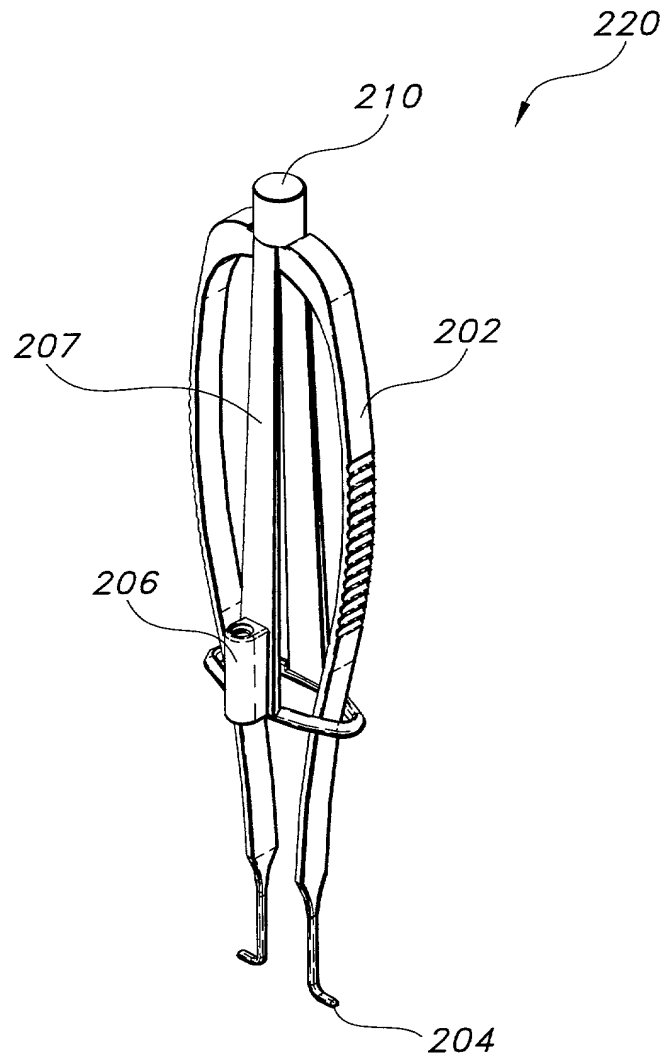
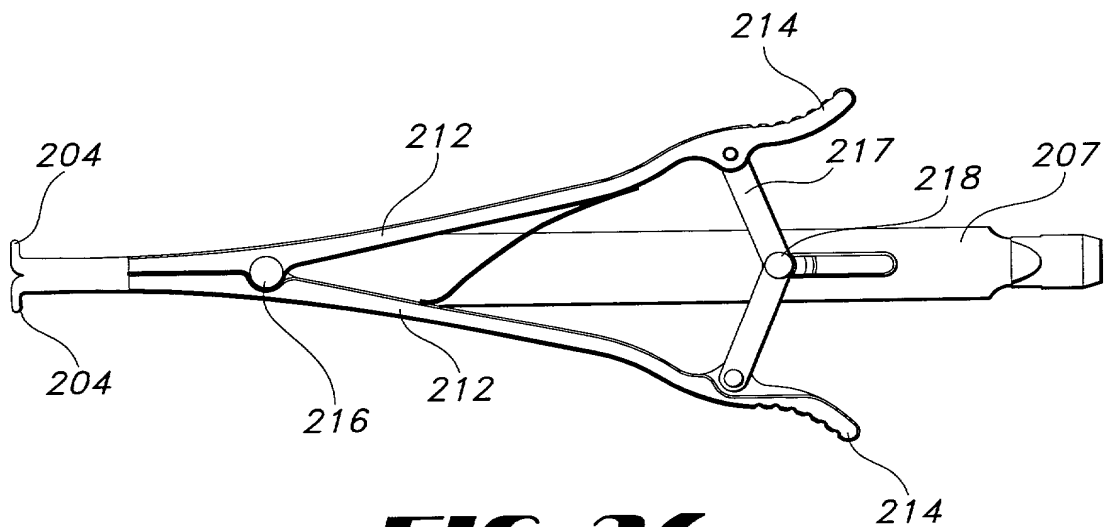
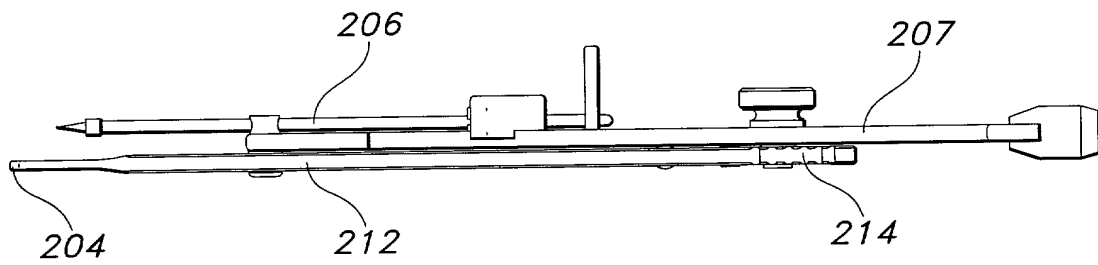
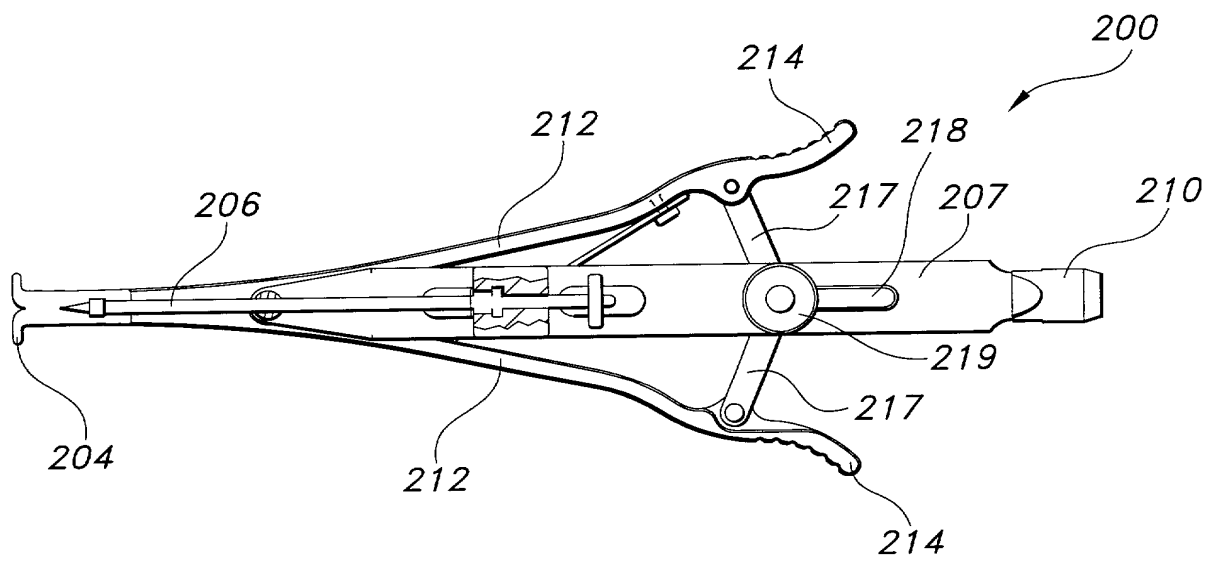


FIG 23

24/82



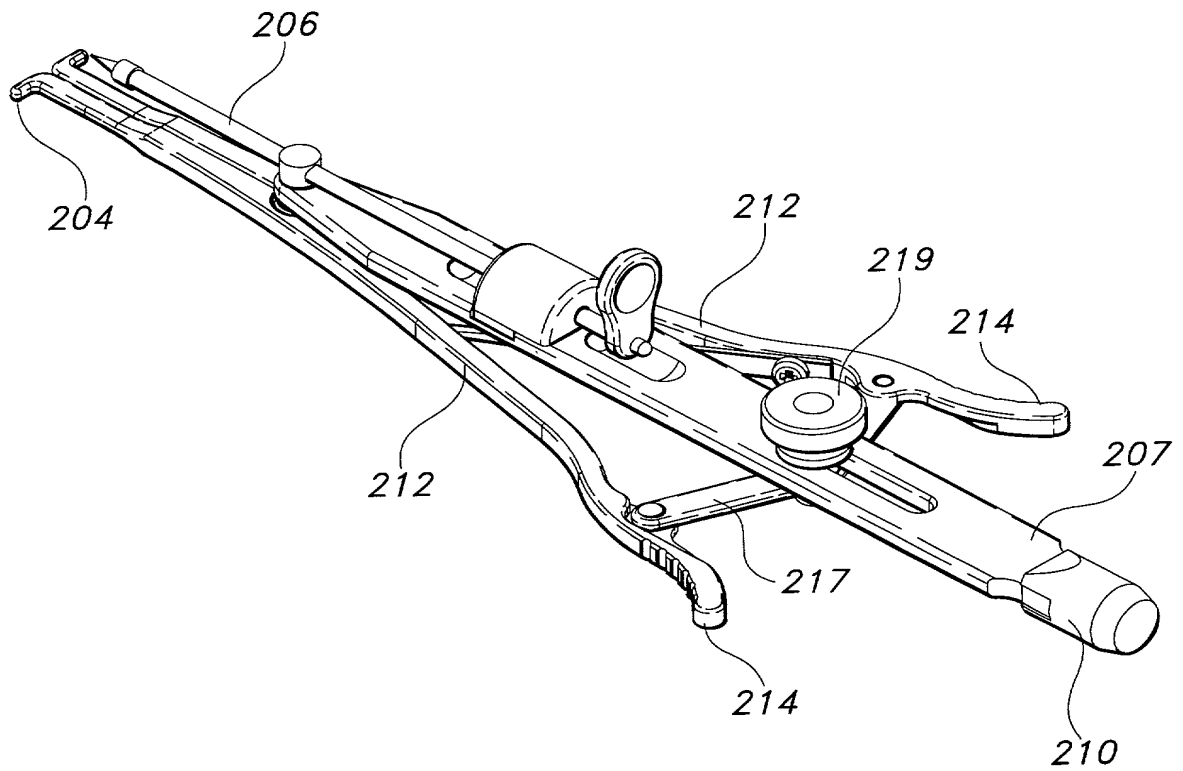


FIG 27

+

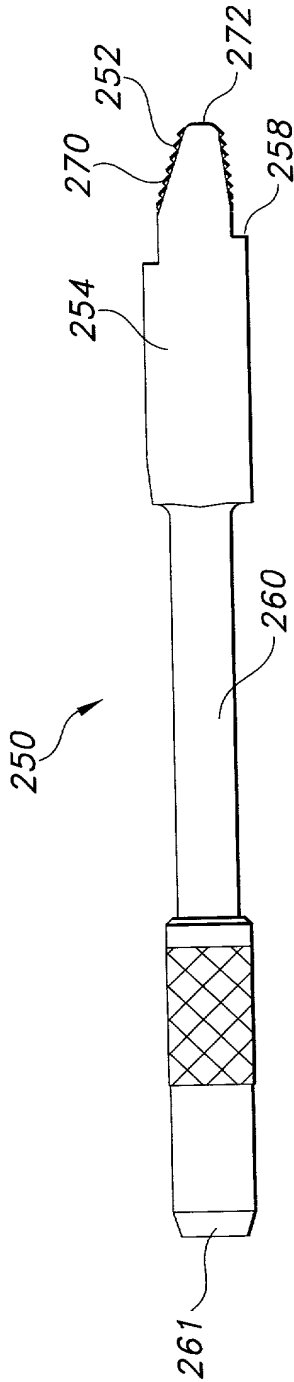


FIG 28B

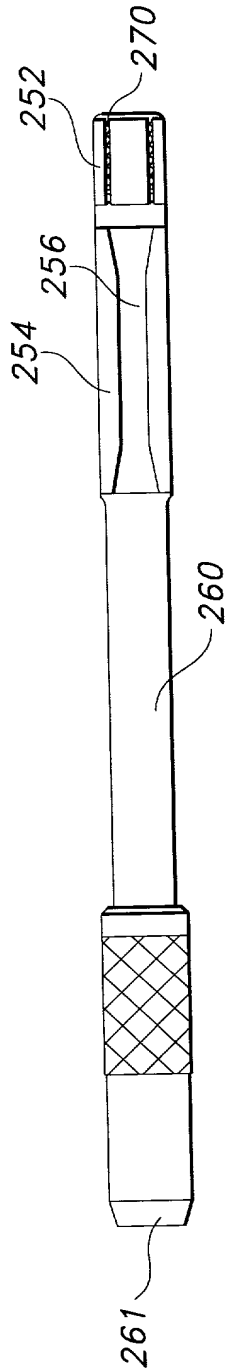


FIG 28A

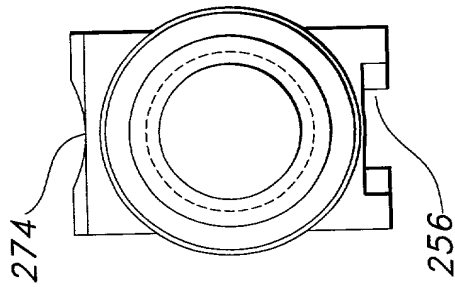


FIG 28C

+

27/82

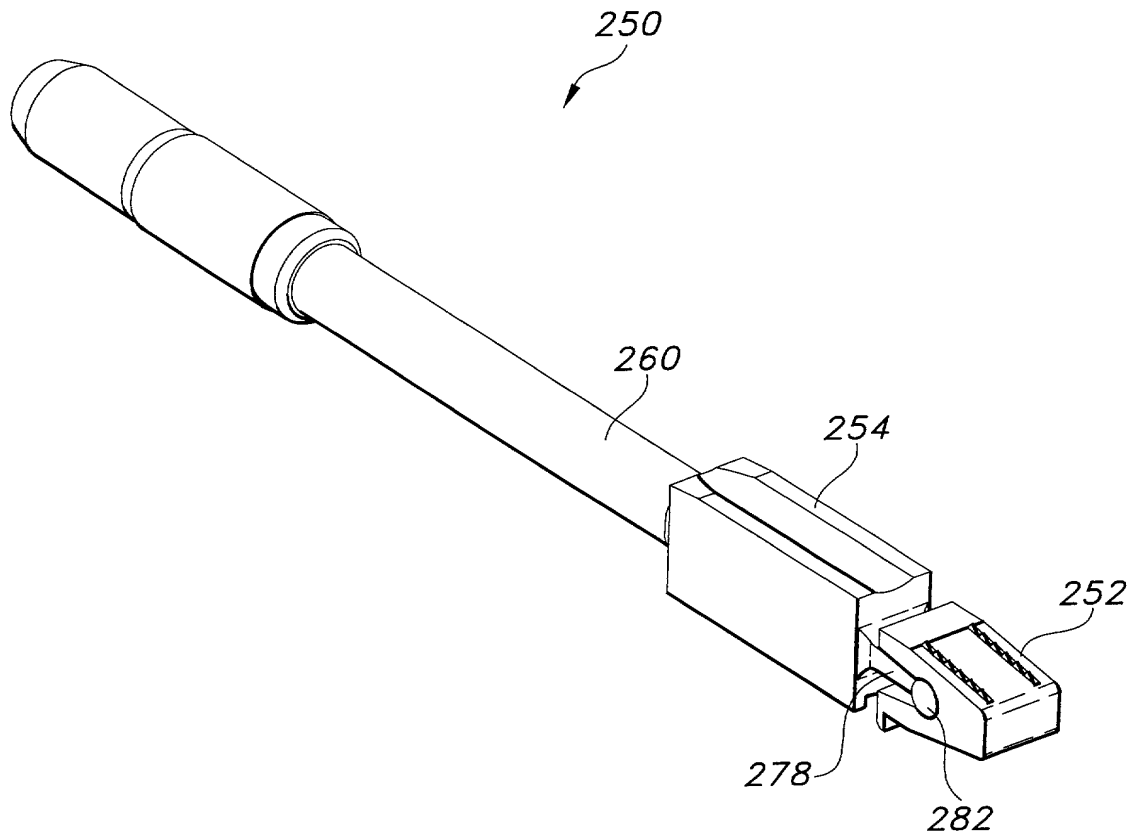
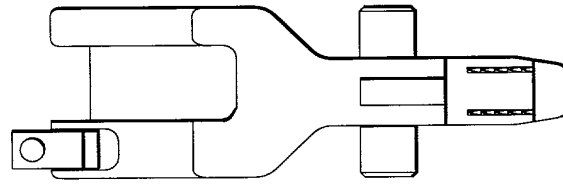
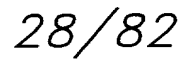
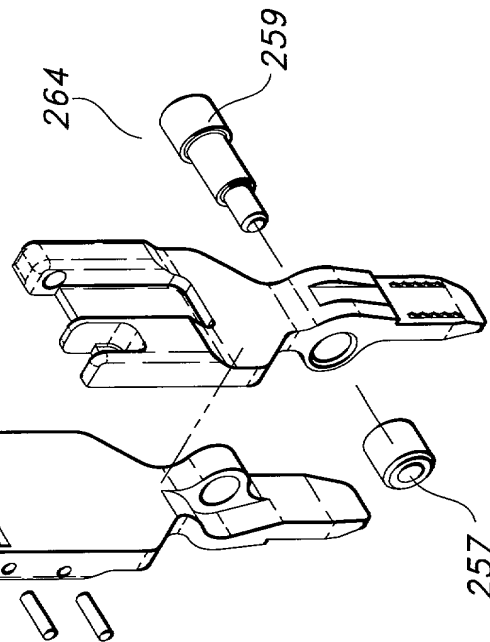
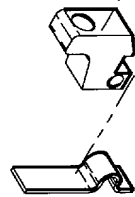
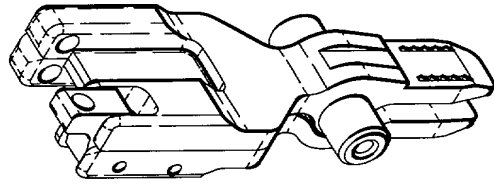


FIG 29



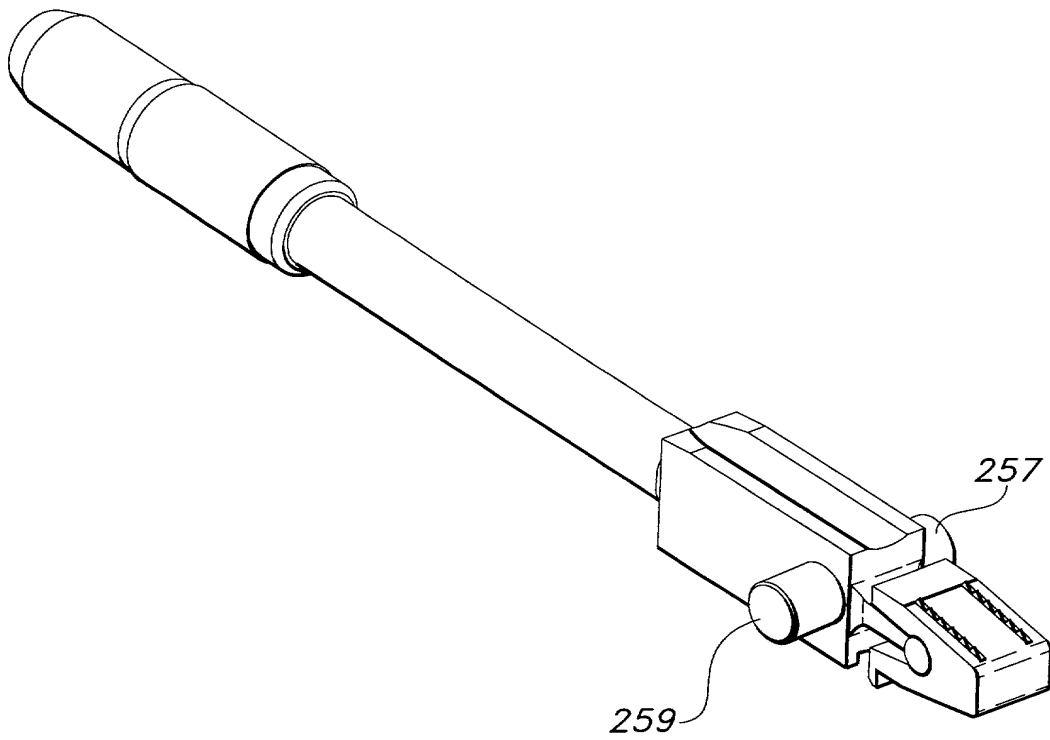


FIG 31

30/82

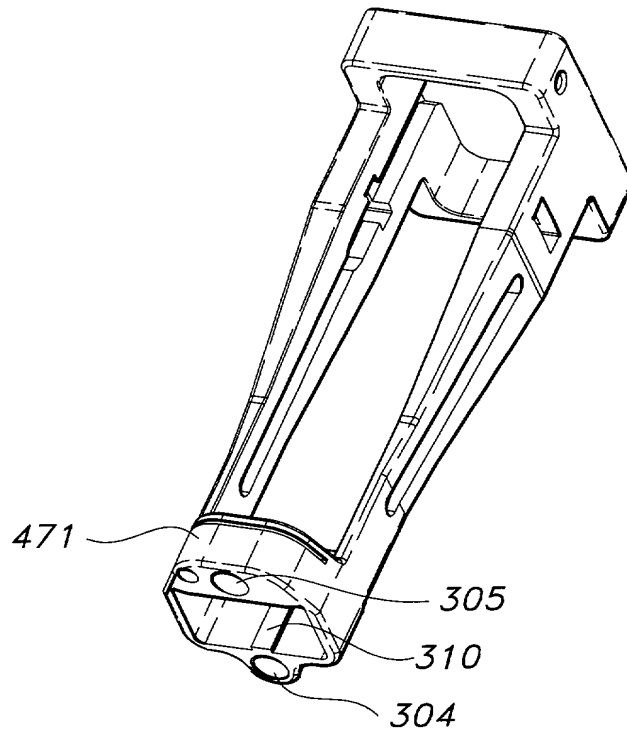


FIG 32A

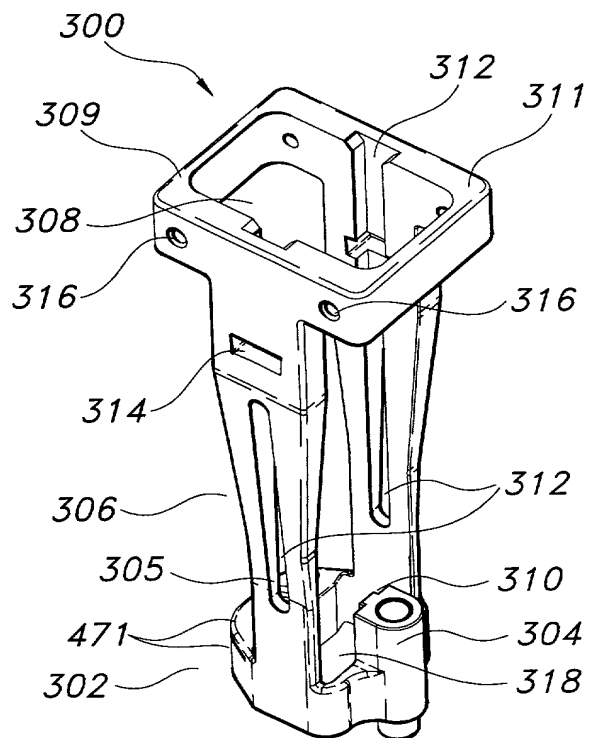


FIG 32B

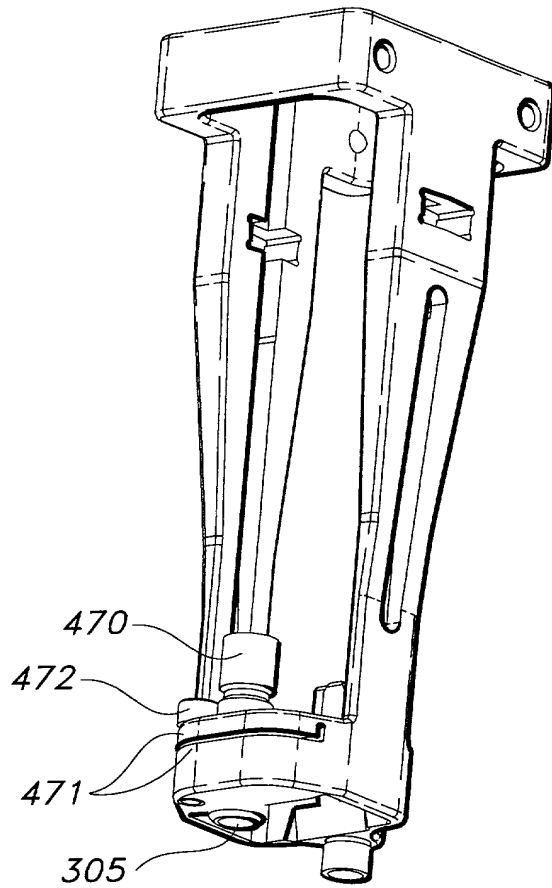


FIG 32C

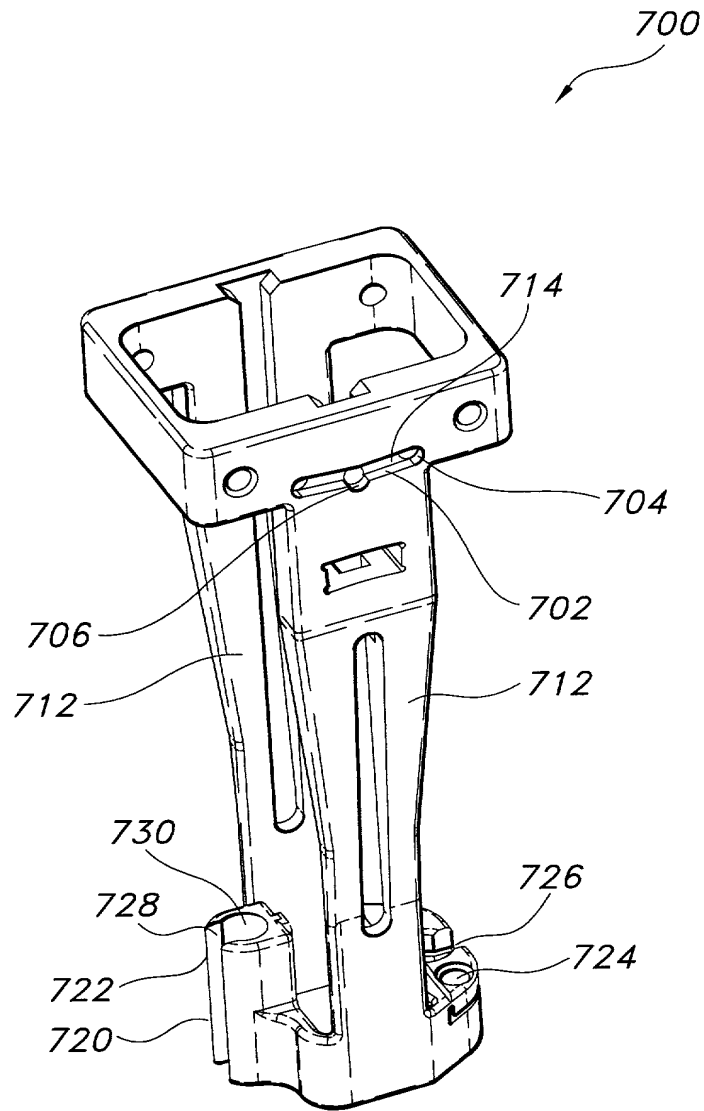


FIG 33A

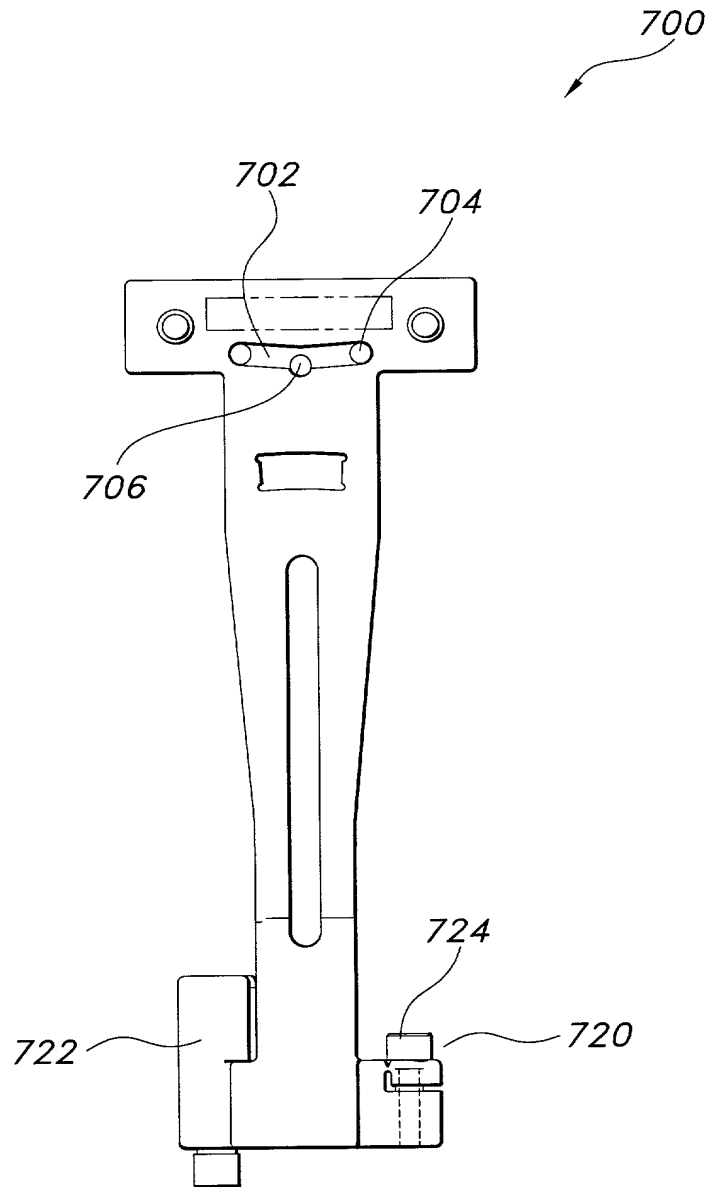


FIG 33B

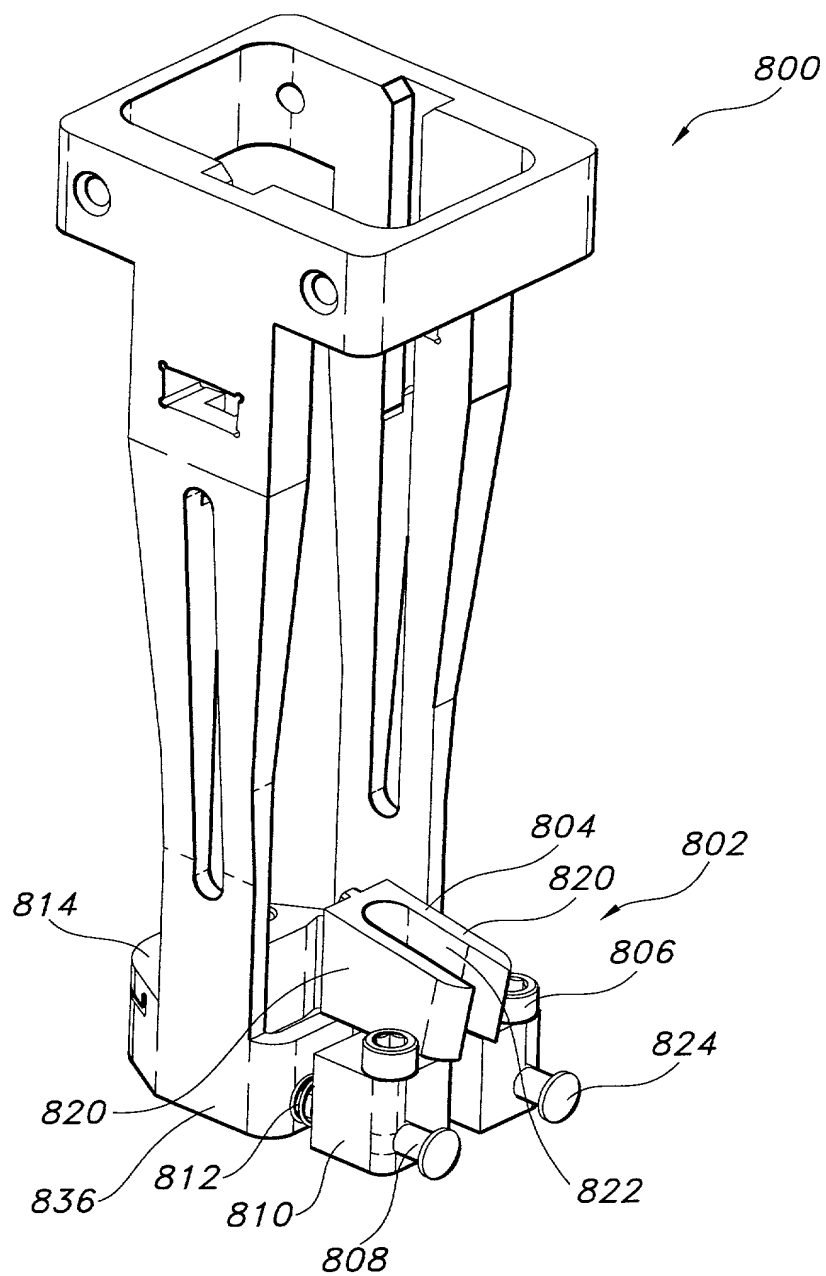


FIG 34A

35/82

800

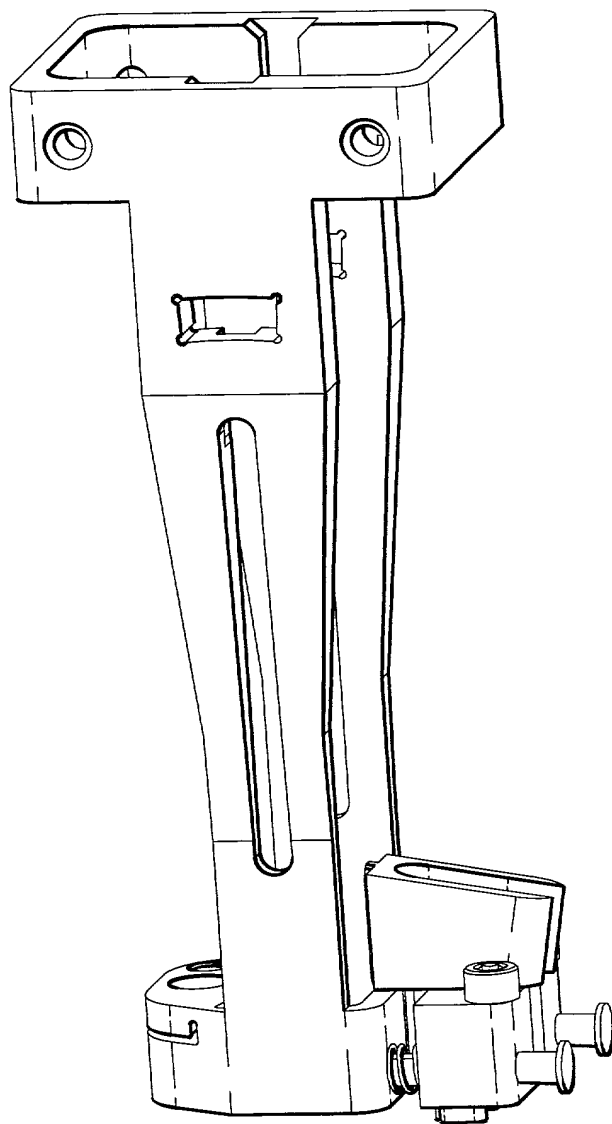


FIG 34B

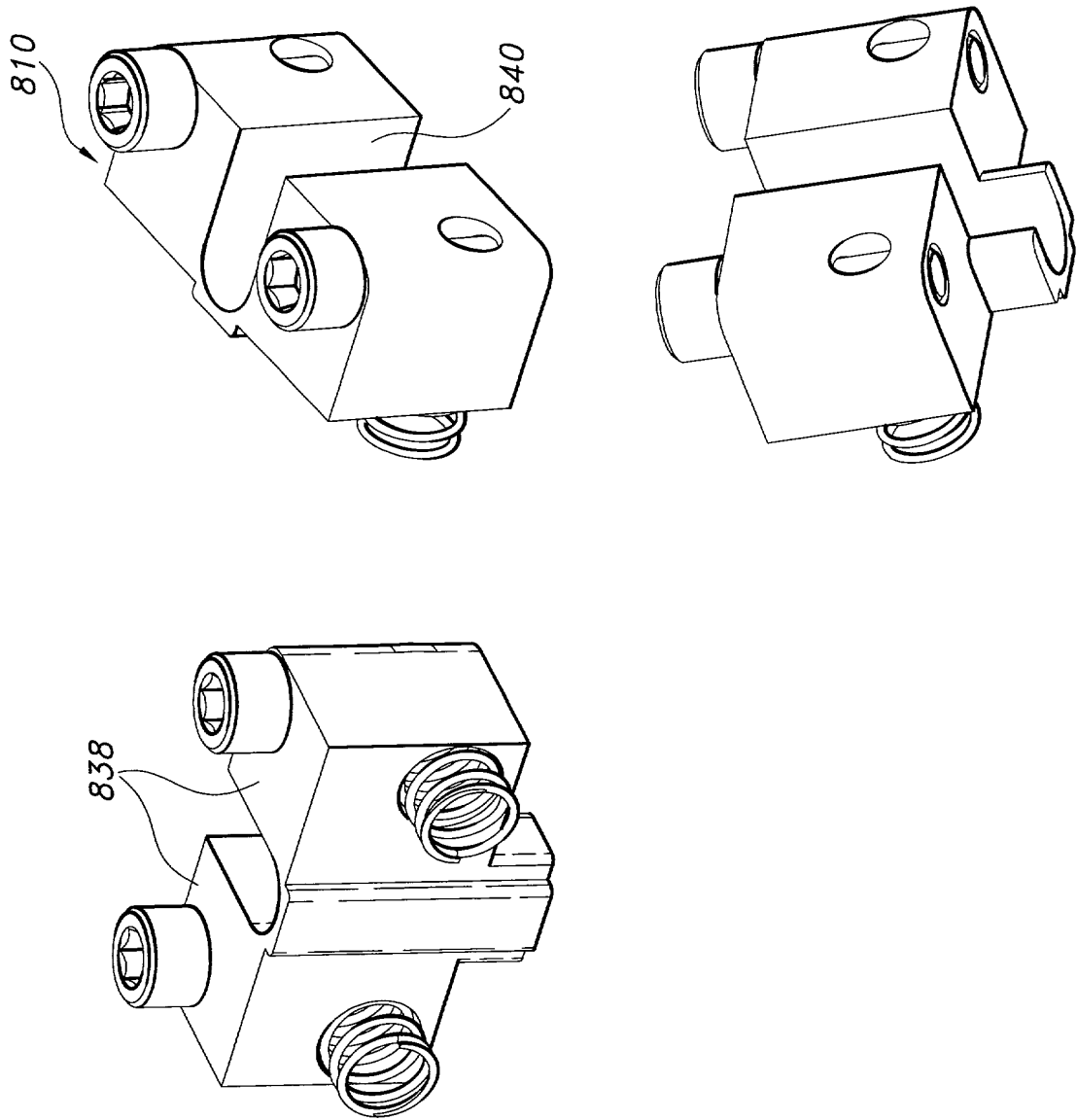


FIG 34C

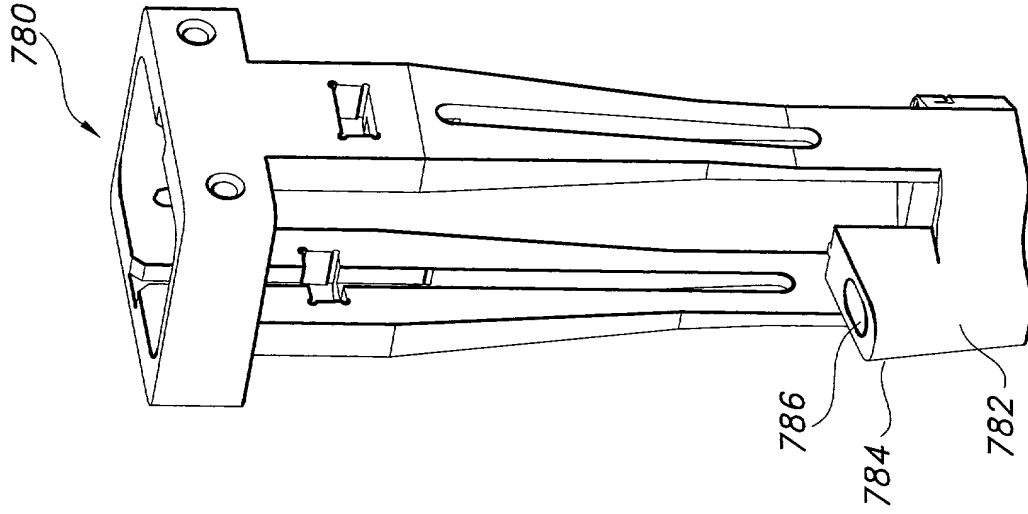
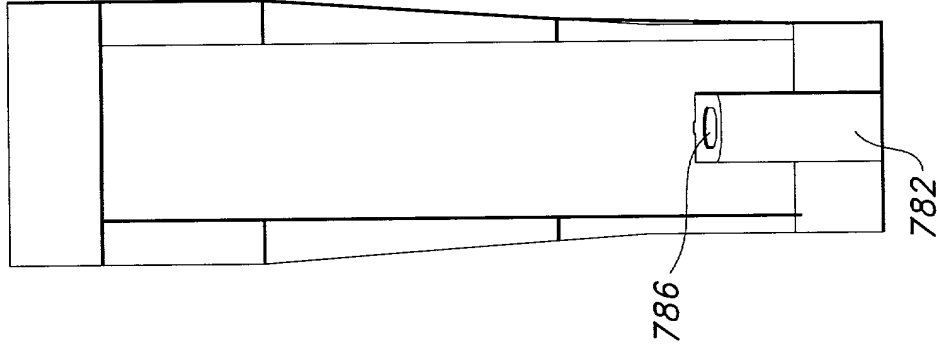
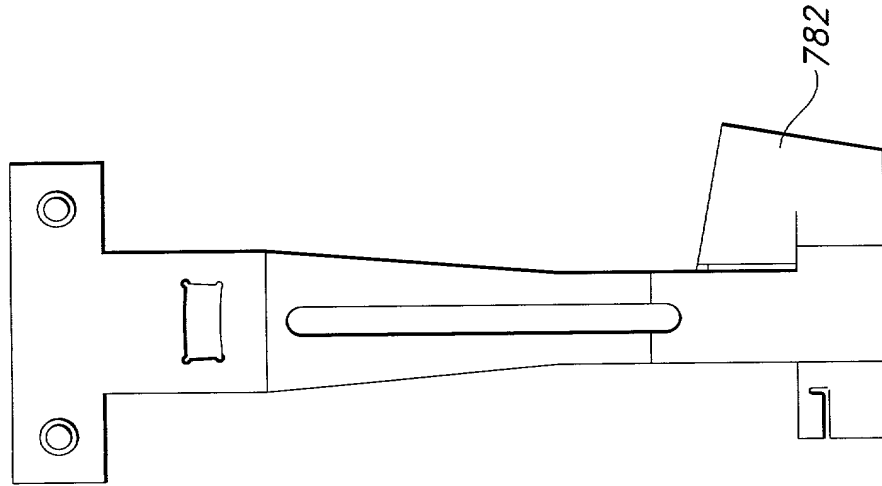


FIG 35A

FIG 35B

FIG 35C



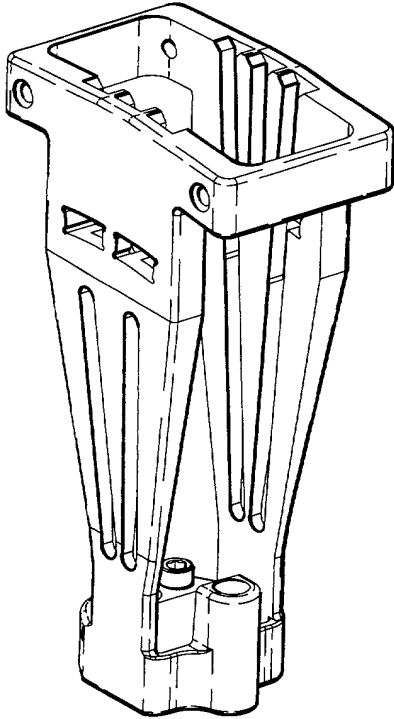


FIG 36C

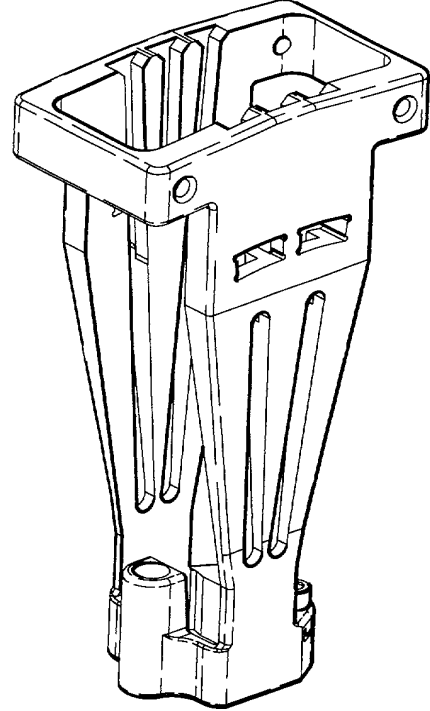


FIG 36D

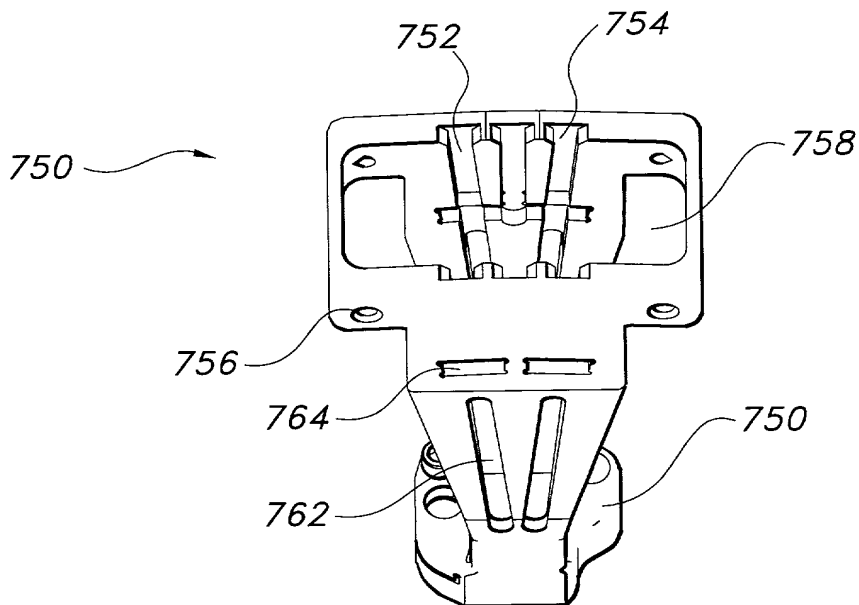


FIG 36A



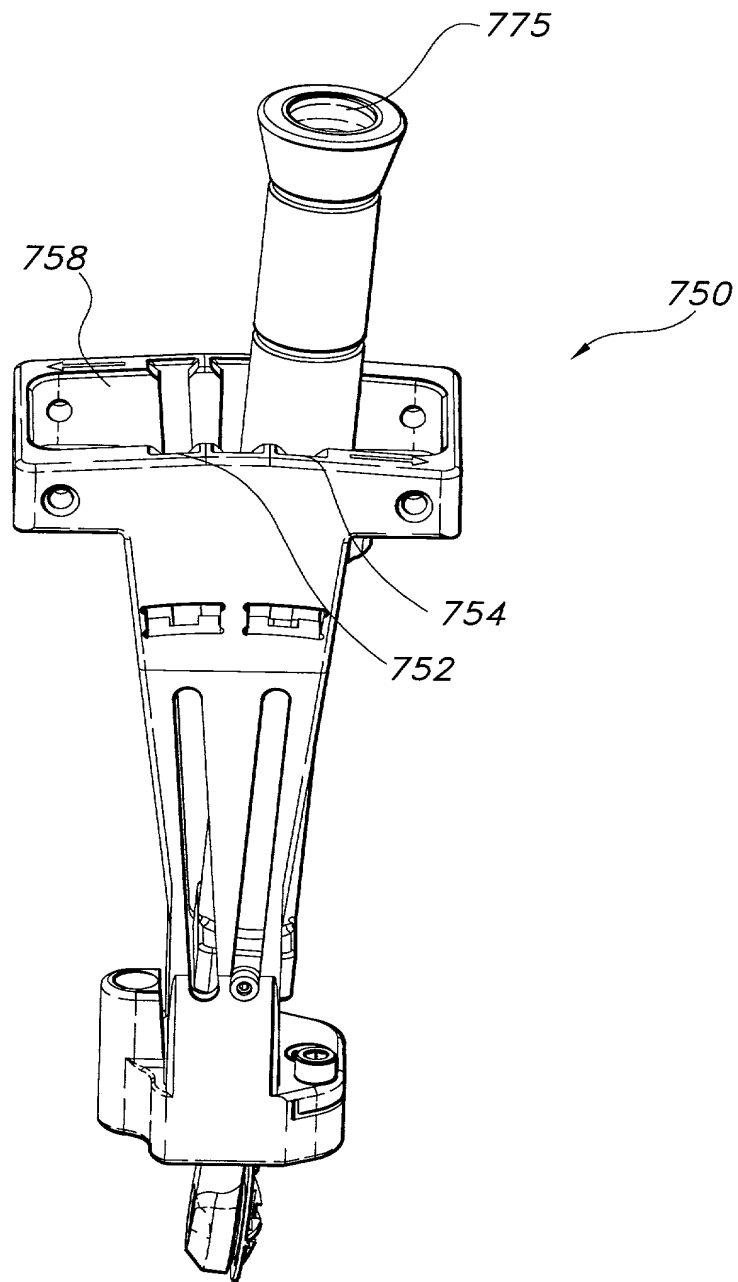


FIG 36B



FIG. 37A

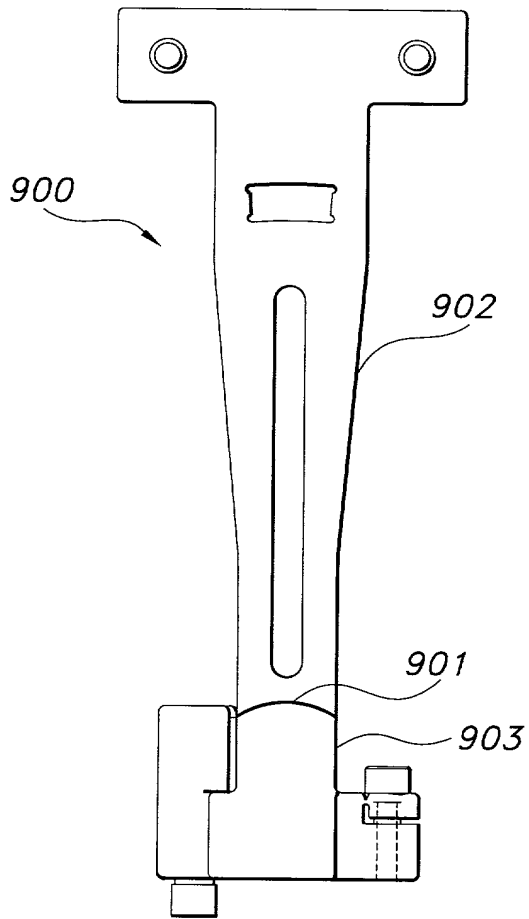


FIG 37A

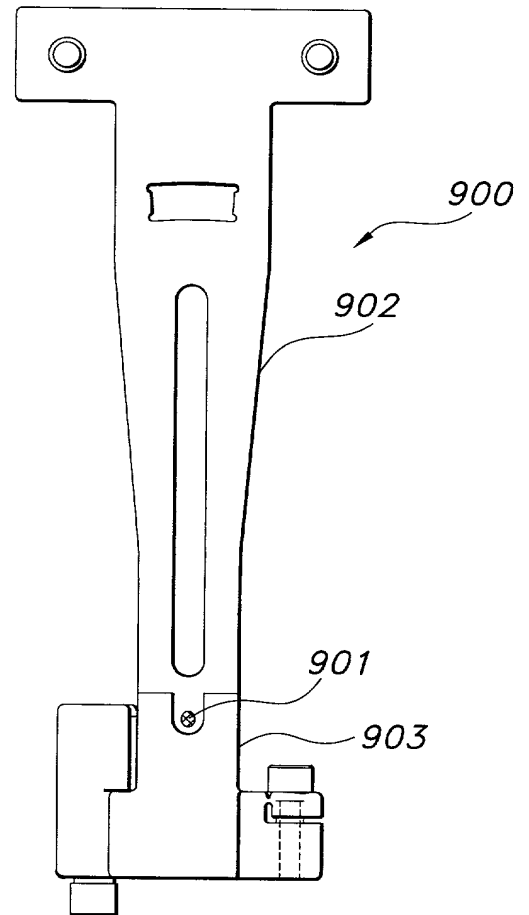


FIG 37B



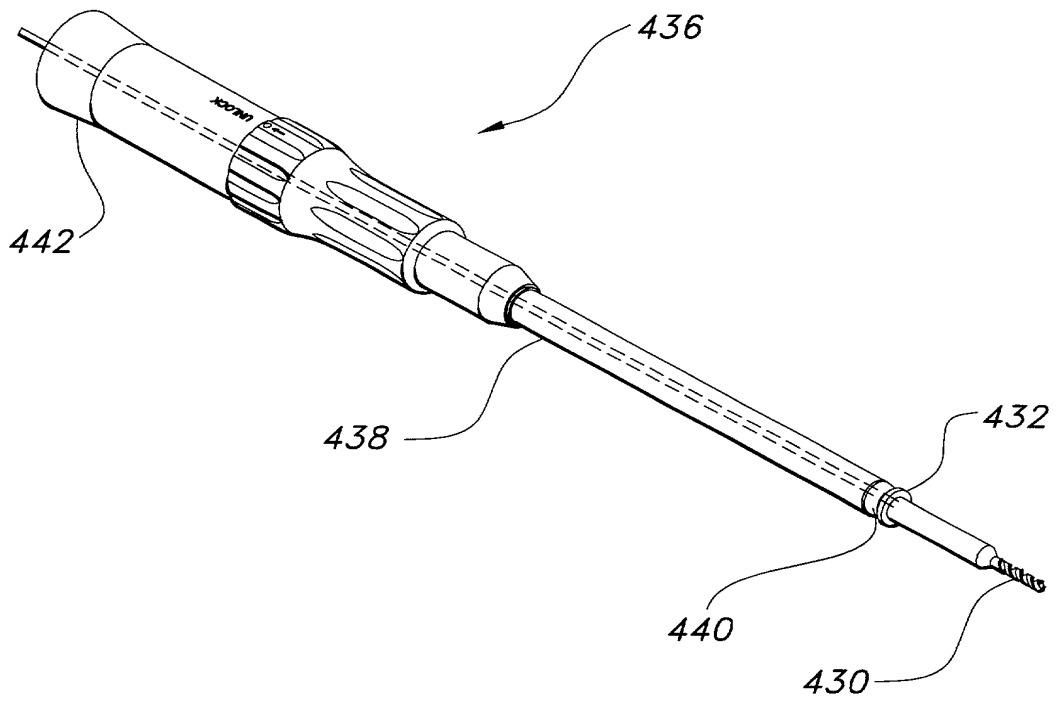


FIG 38

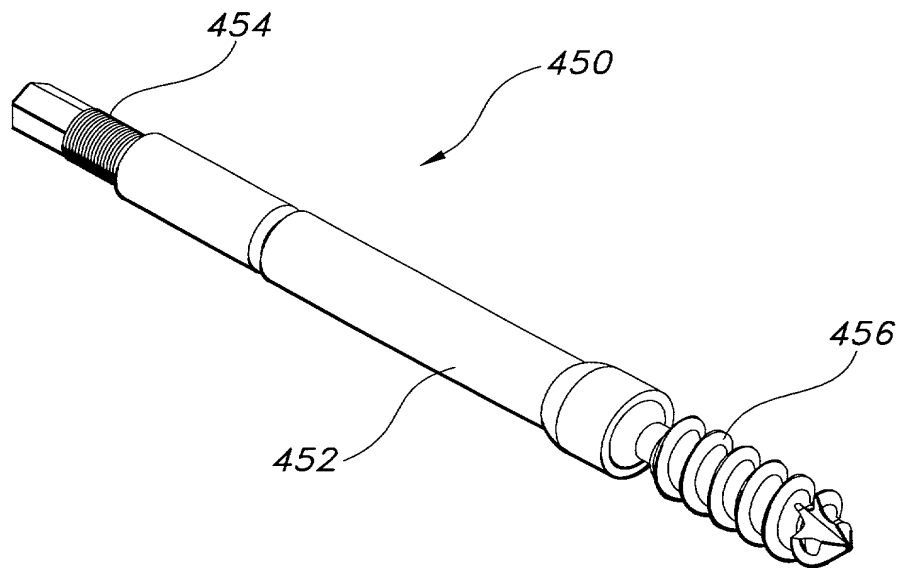


FIG 39A



FIG. 39B

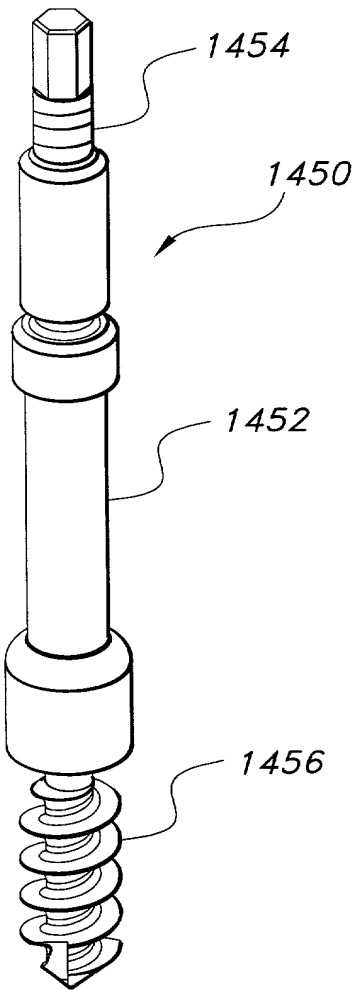


FIG 39B

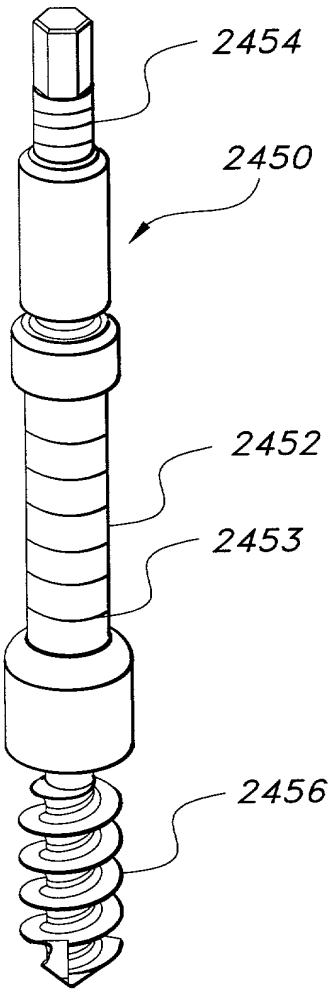


FIG 39C



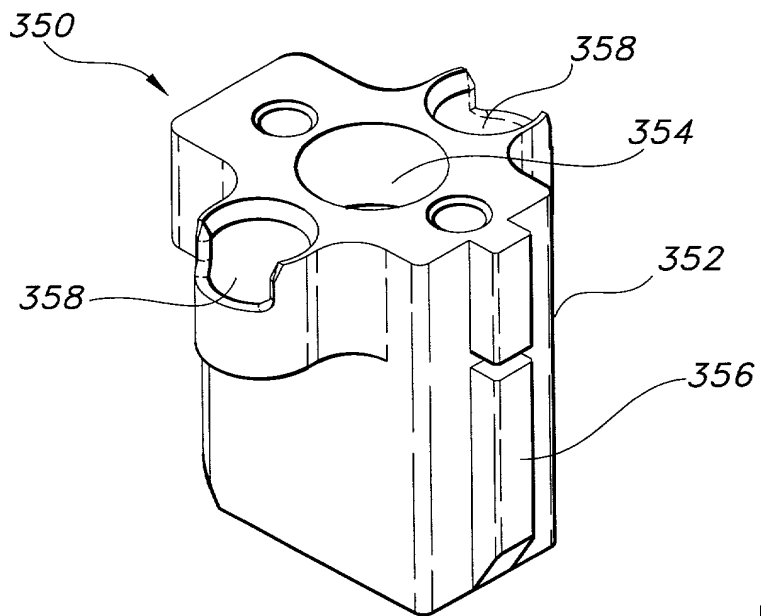


FIG 40A

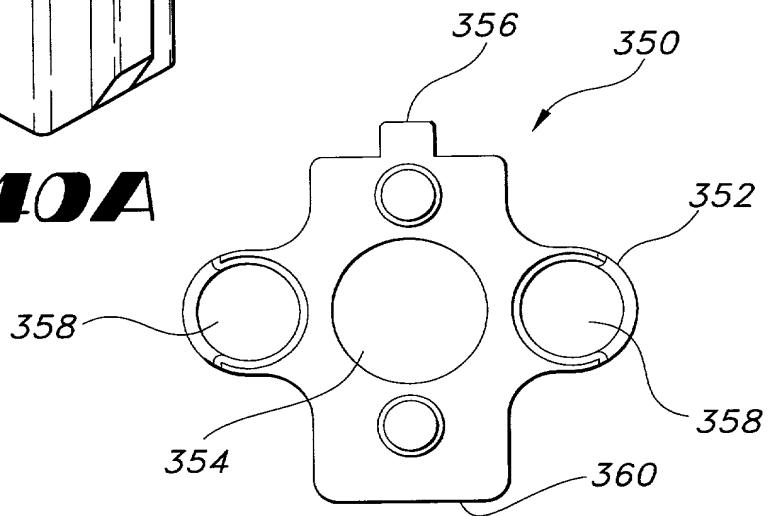


FIG 40B

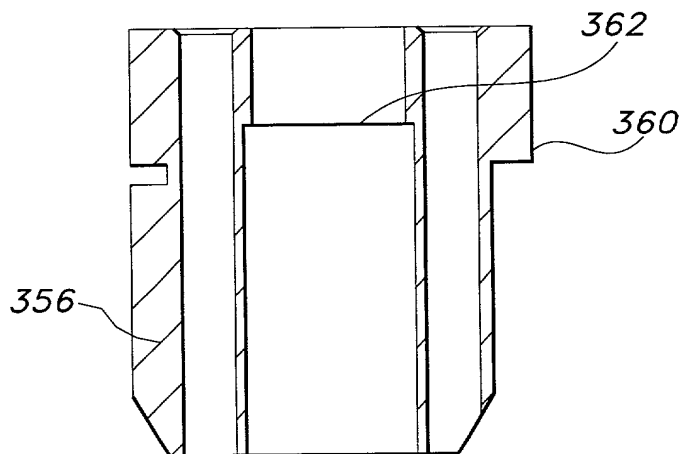


FIG 40C

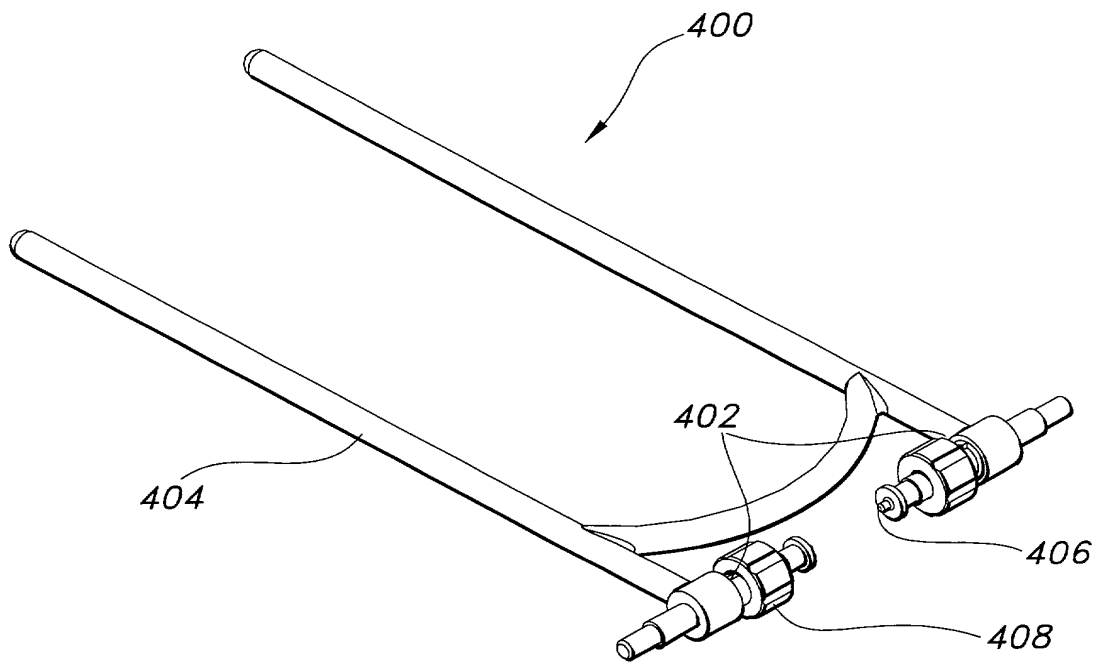


FIG 41A

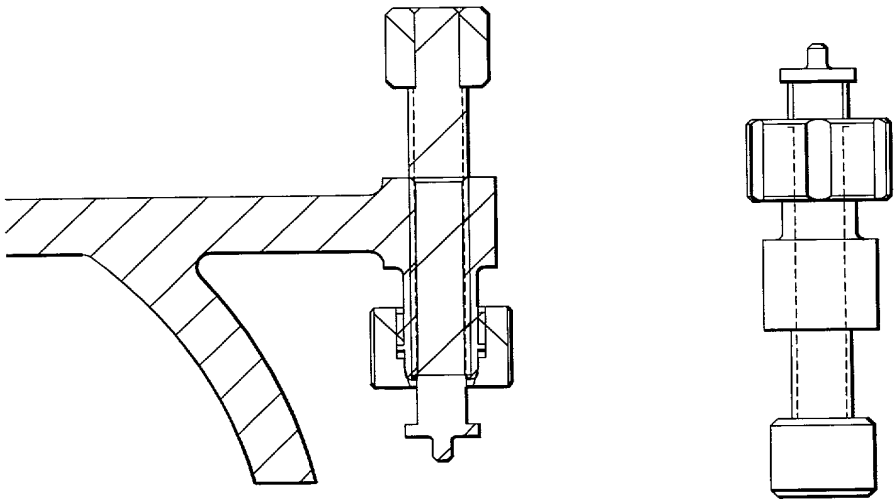


FIG 41B



www.4mat.com

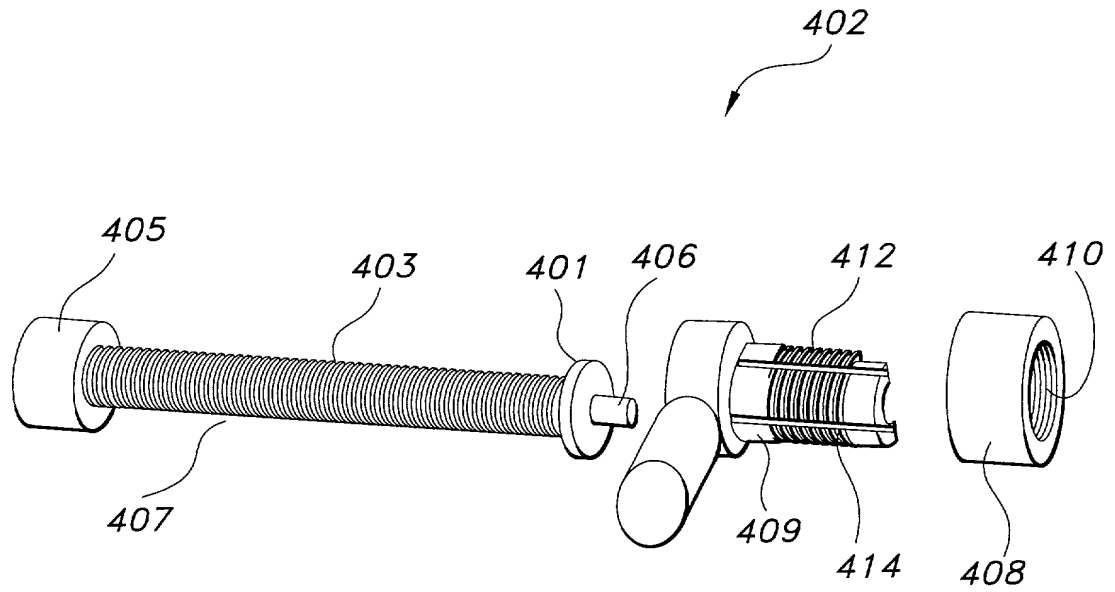


FIG 41C

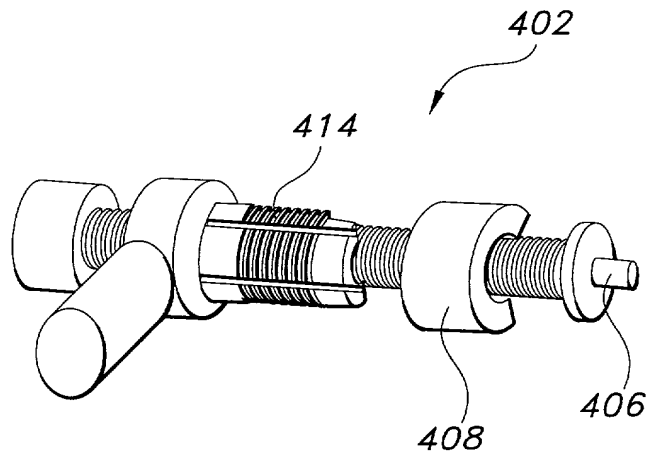


FIG 41D



FIG 42

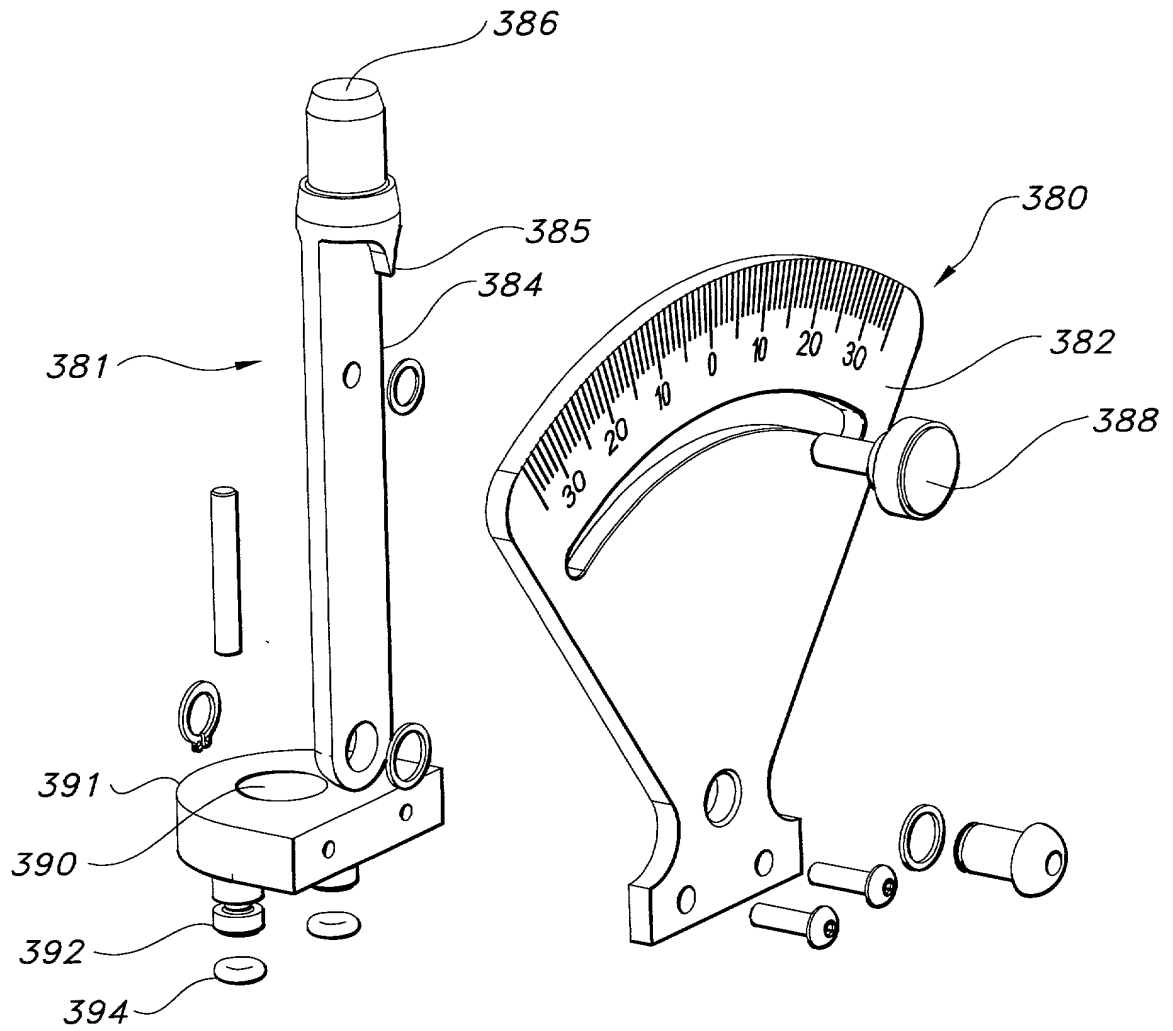


FIG 43



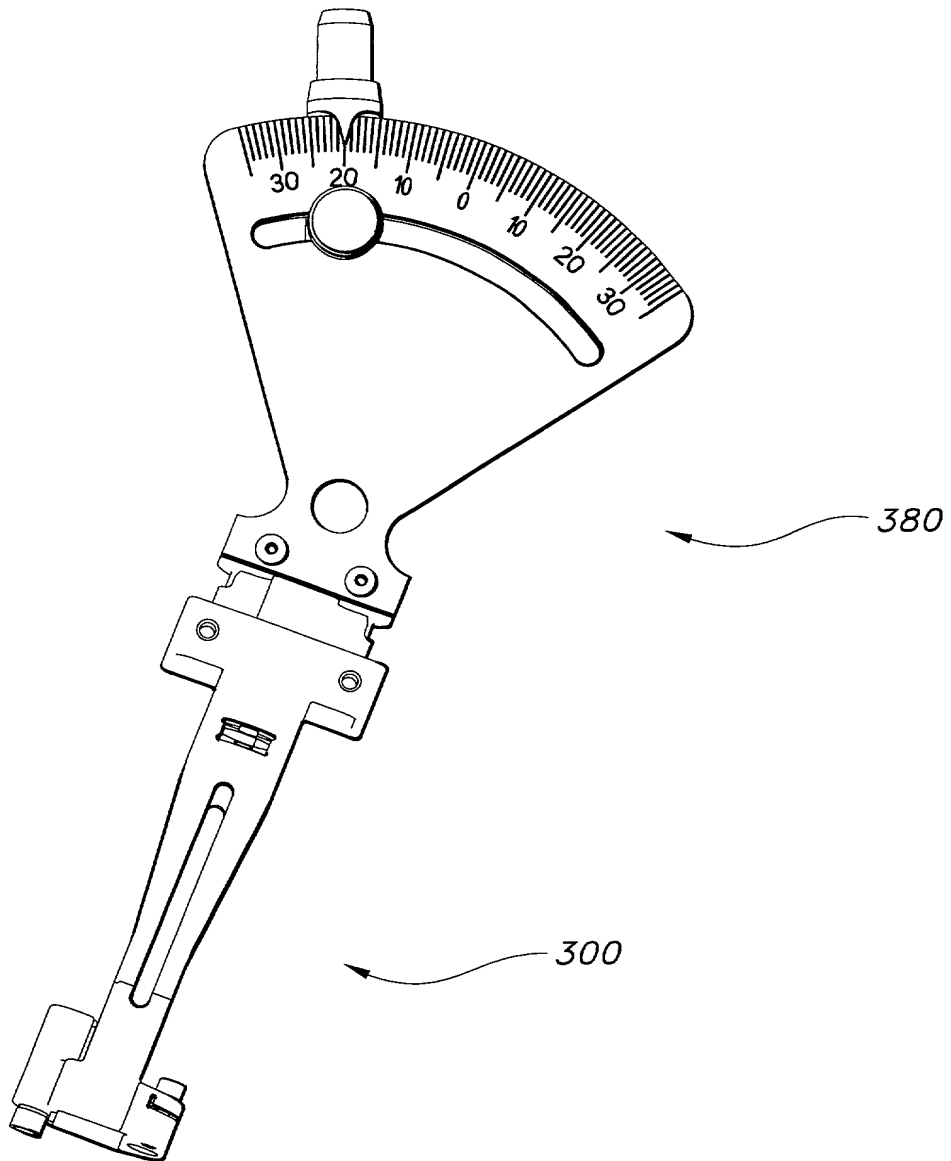


FIG 44

51/82

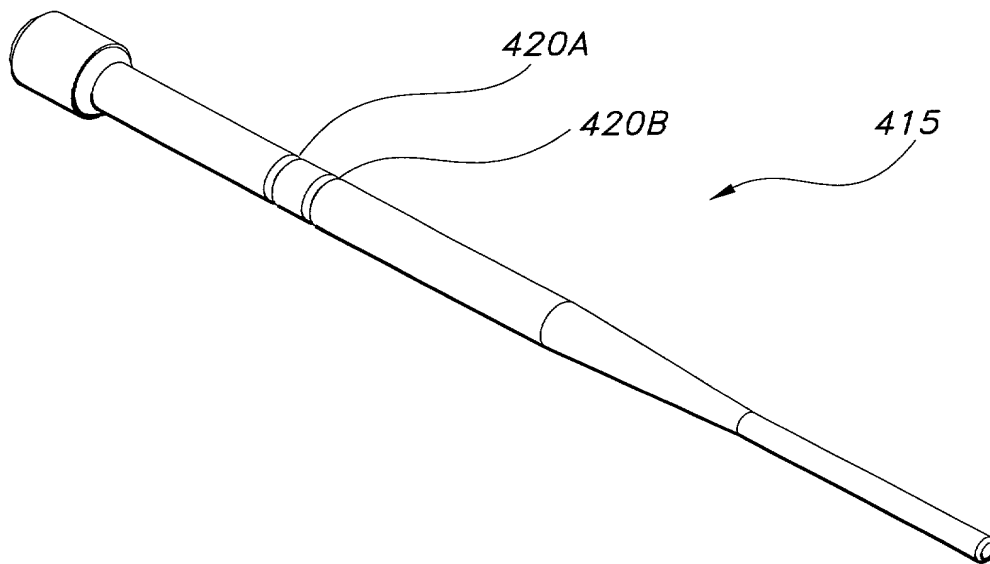


FIG 45

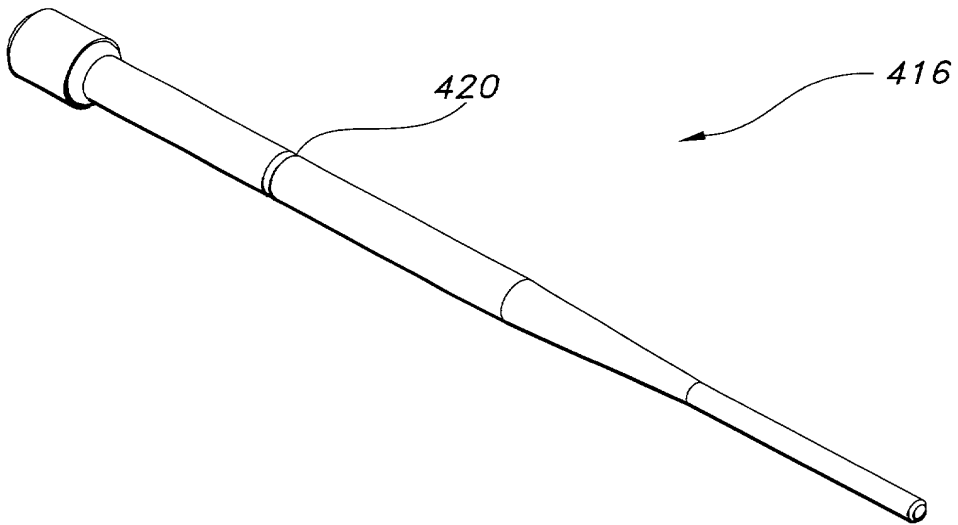


FIG 46

53/82

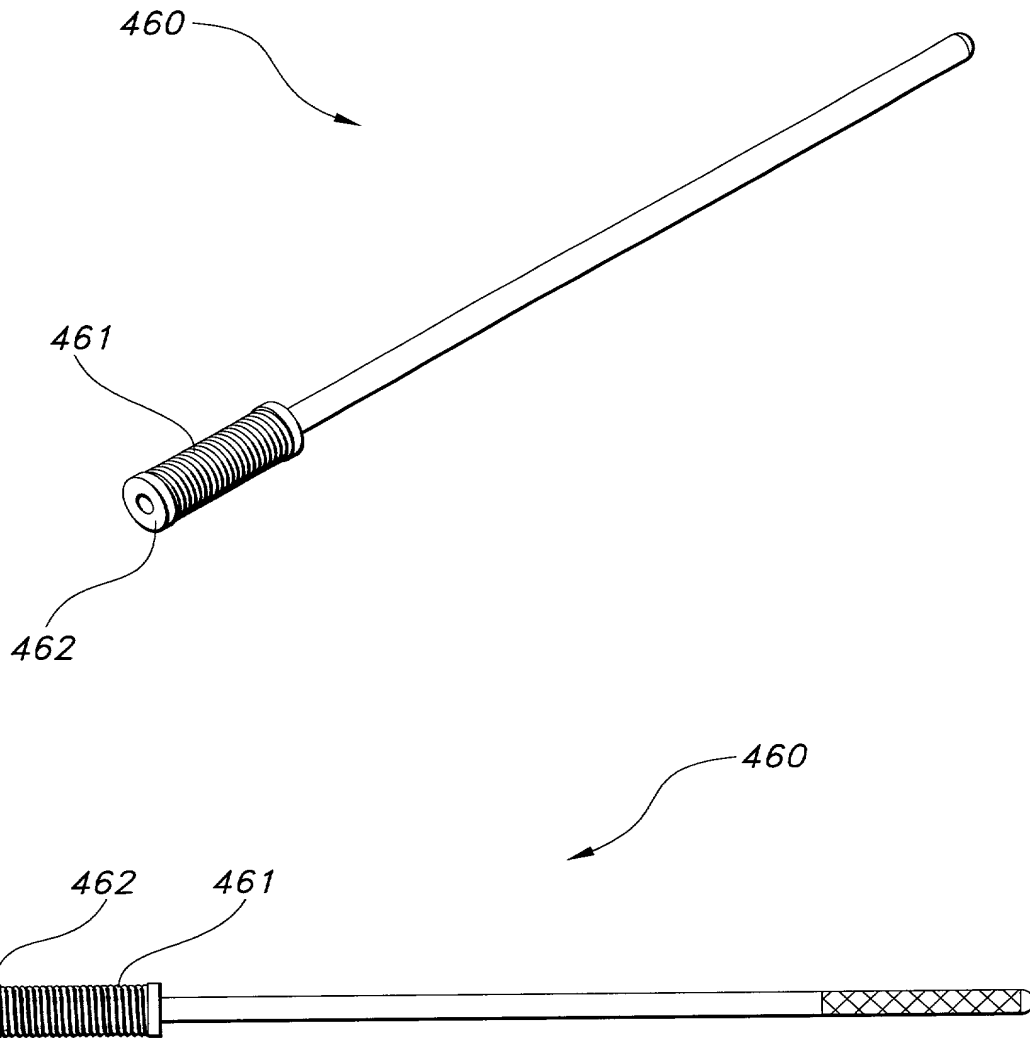


FIG 47

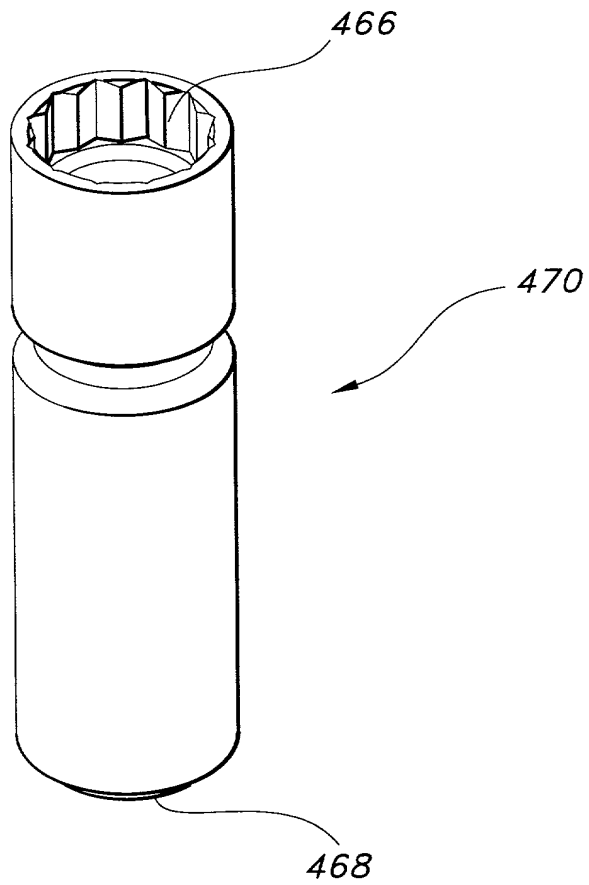


FIG 48



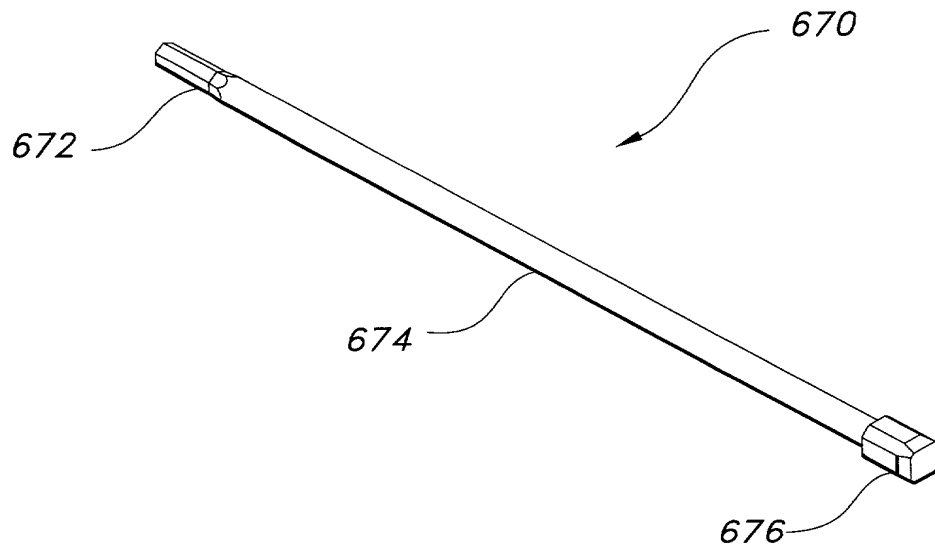


FIG 49A

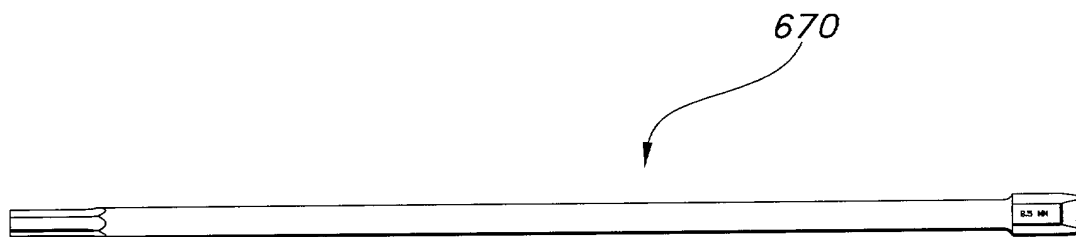


FIG 49B

56/82

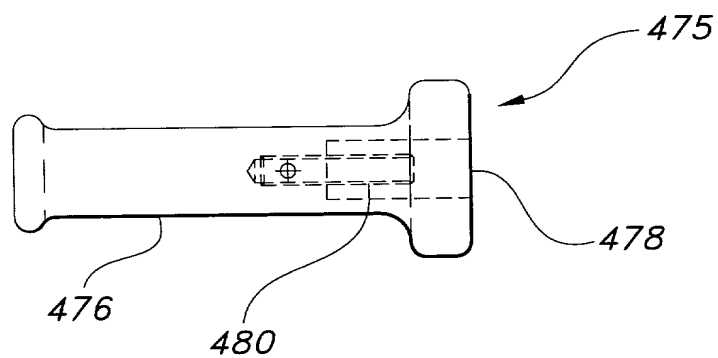


FIG 50A

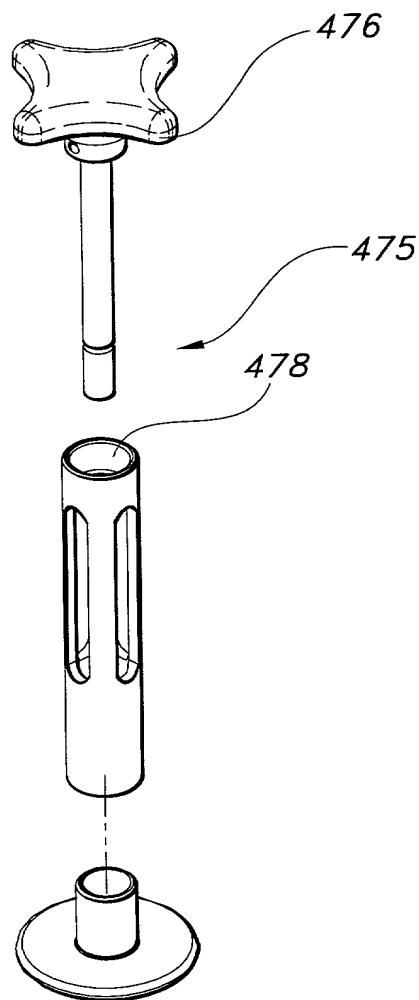


FIG 50B

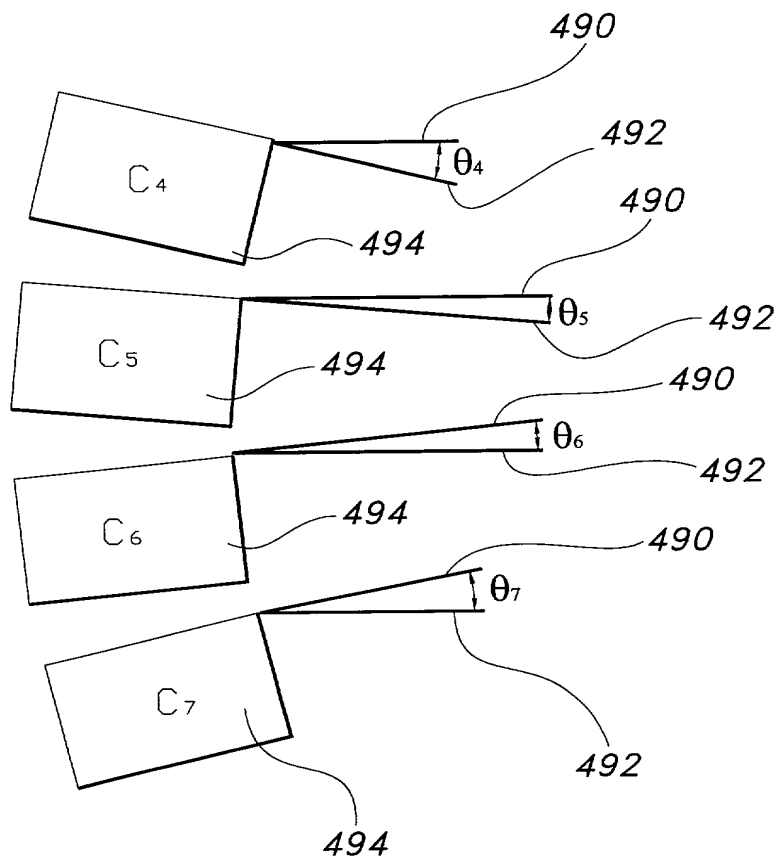
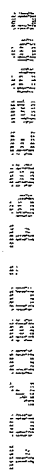


FIG 51



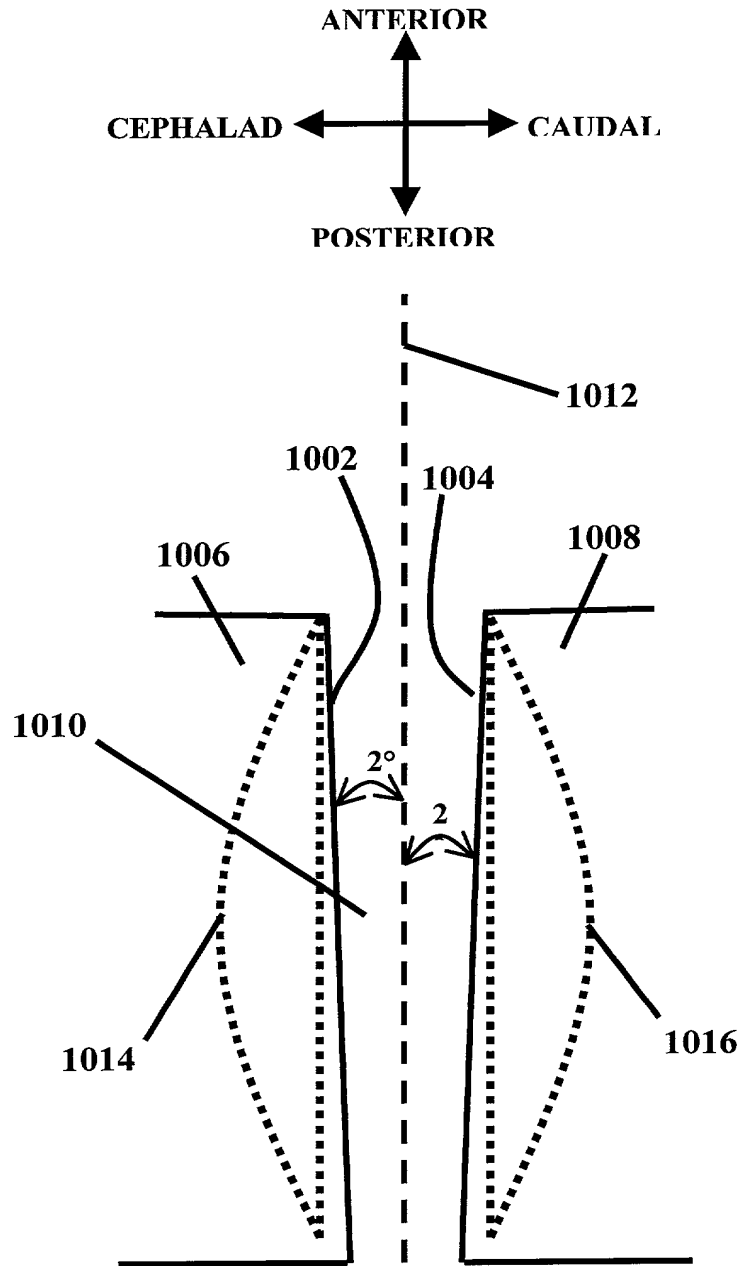


FIGURE 53

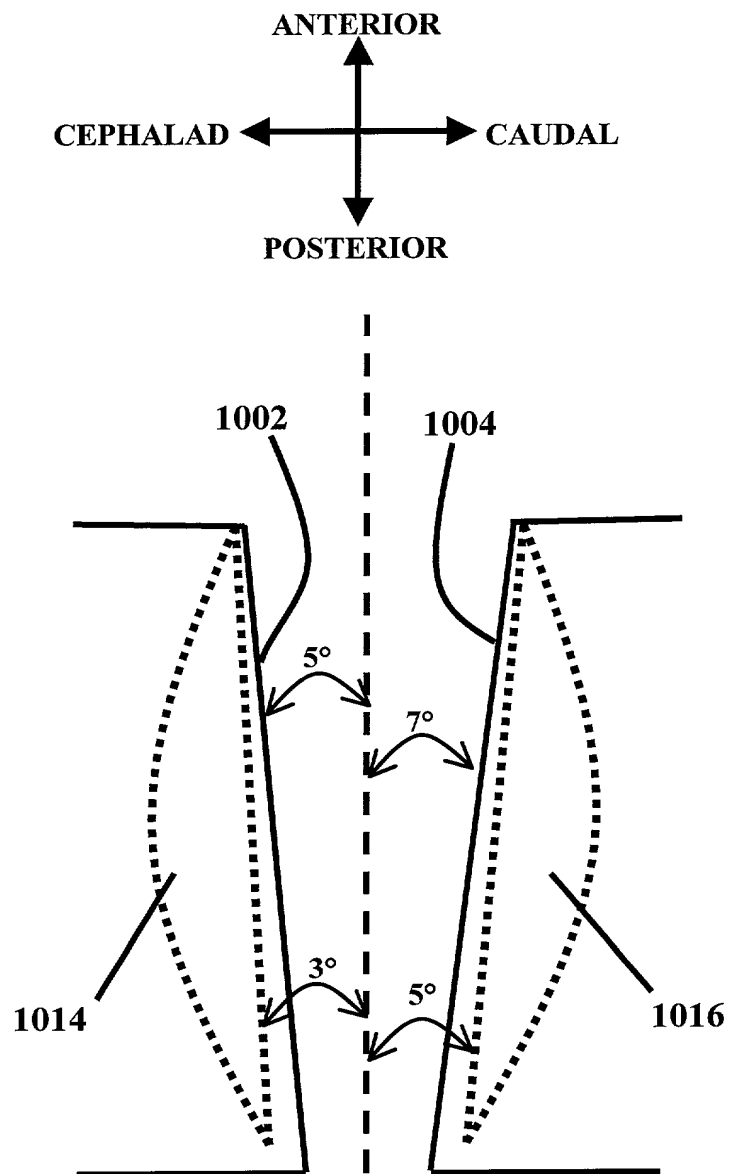


FIGURE 54



FIG. 55A

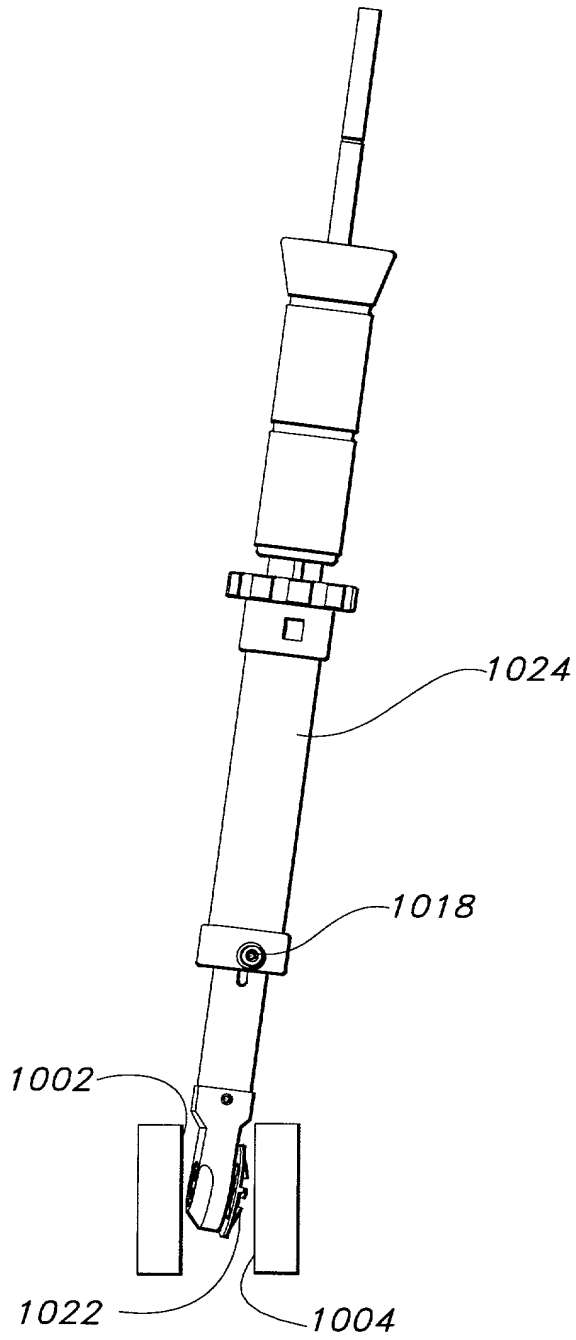


FIG 55A

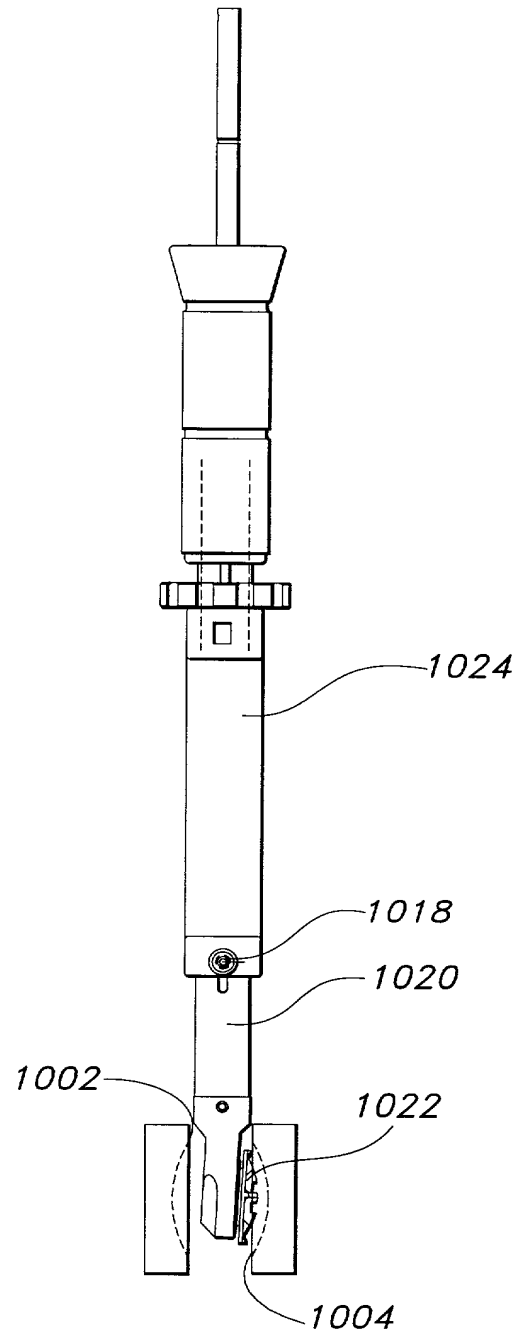


FIG 55B



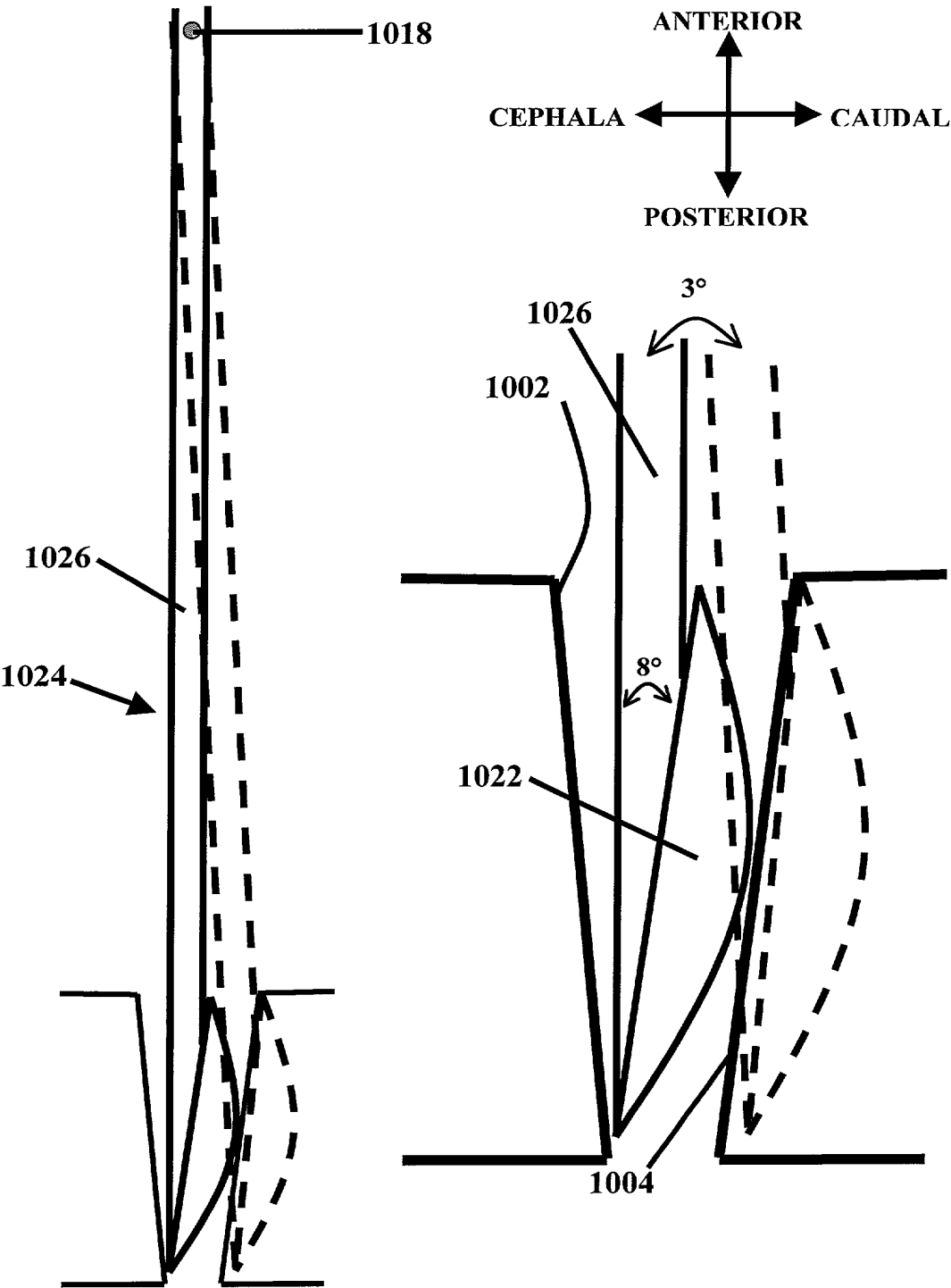


FIGURE 56

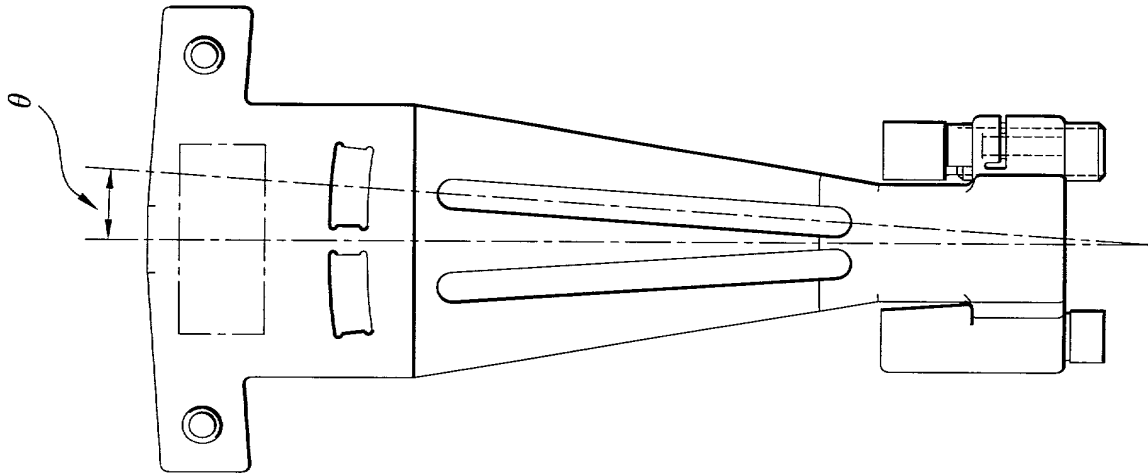


FIG 57B

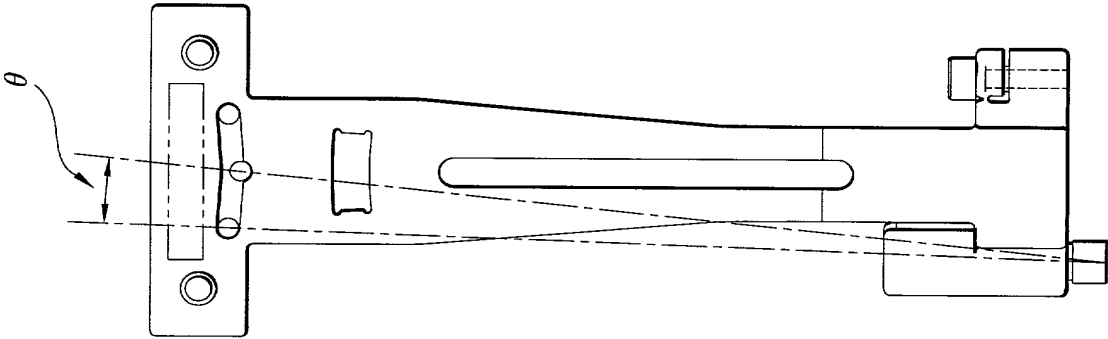


FIG 57A

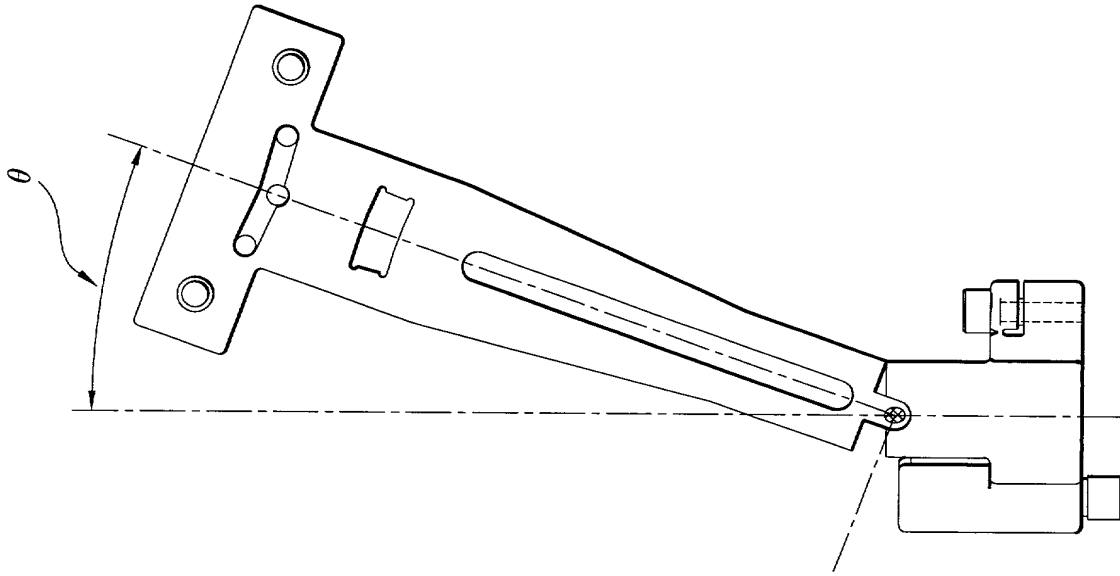
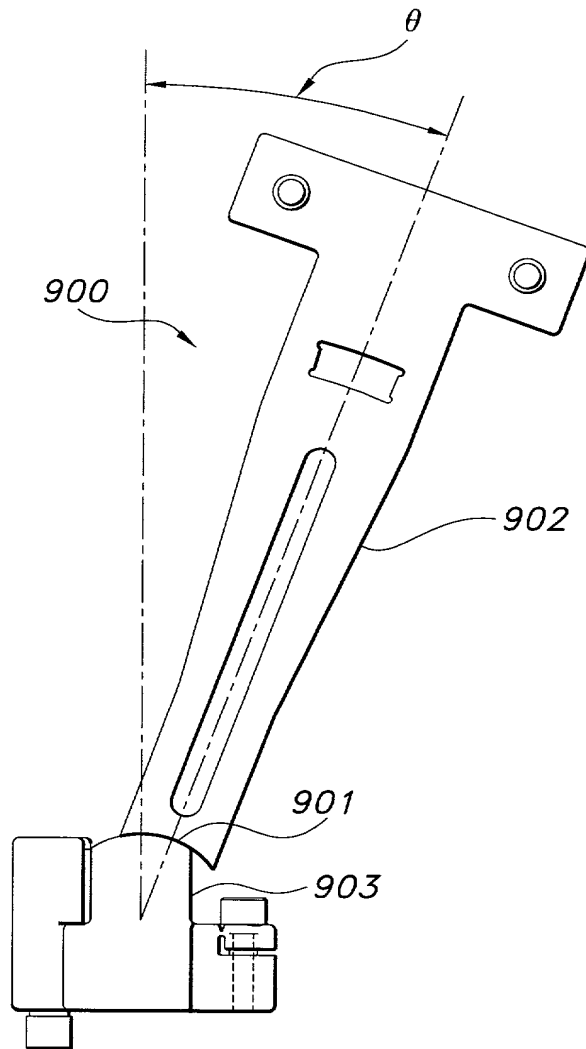


FIG 57C



**FIG 57D**

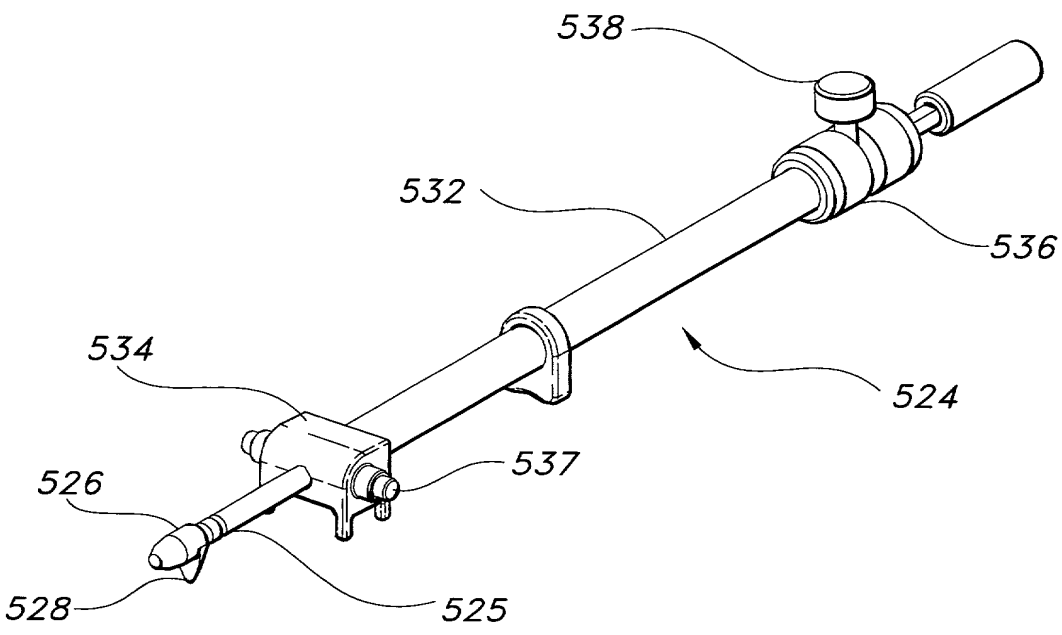


FIG 58



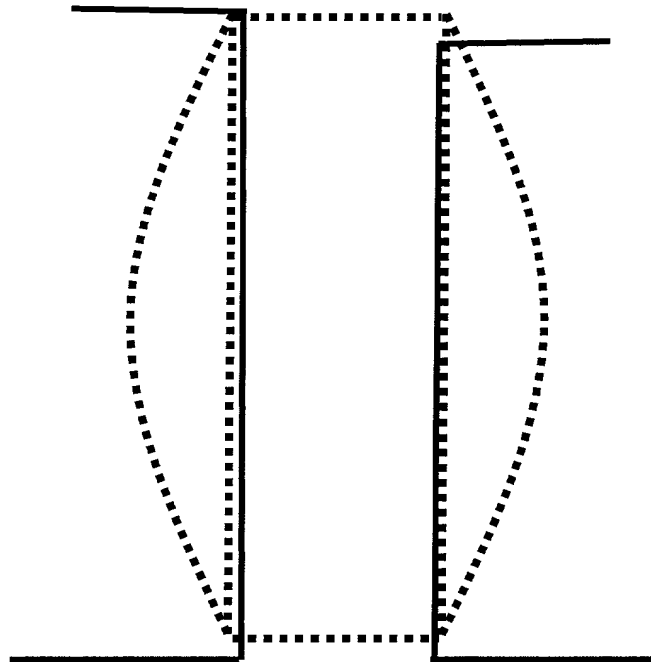
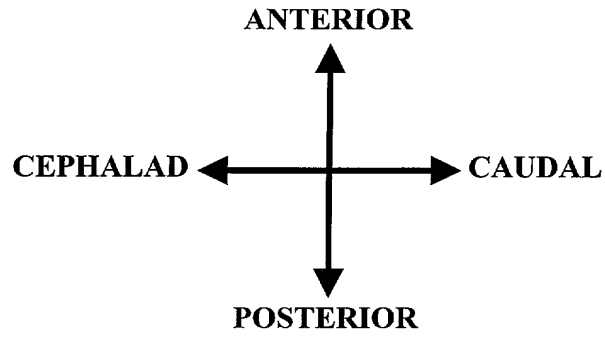


FIGURE 59

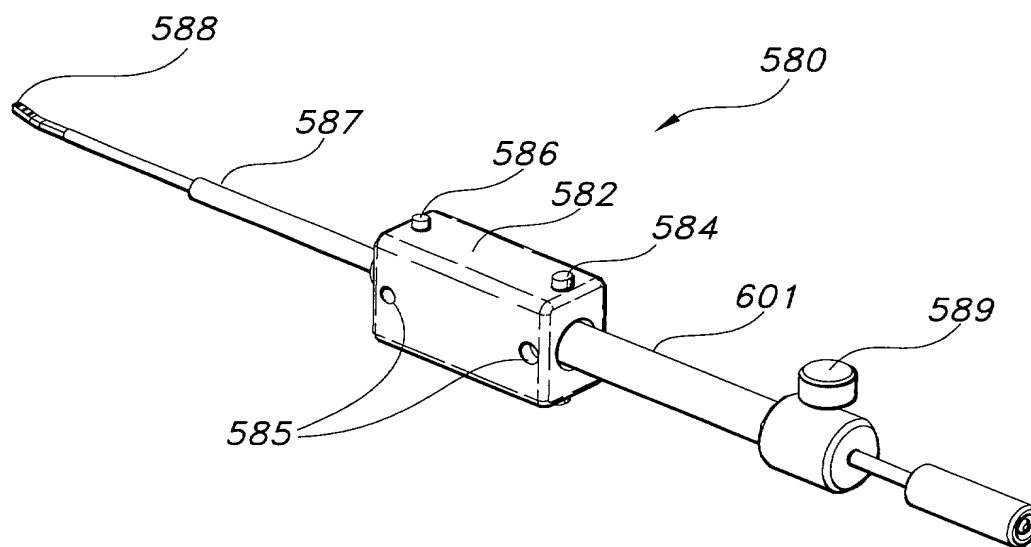


FIG 60

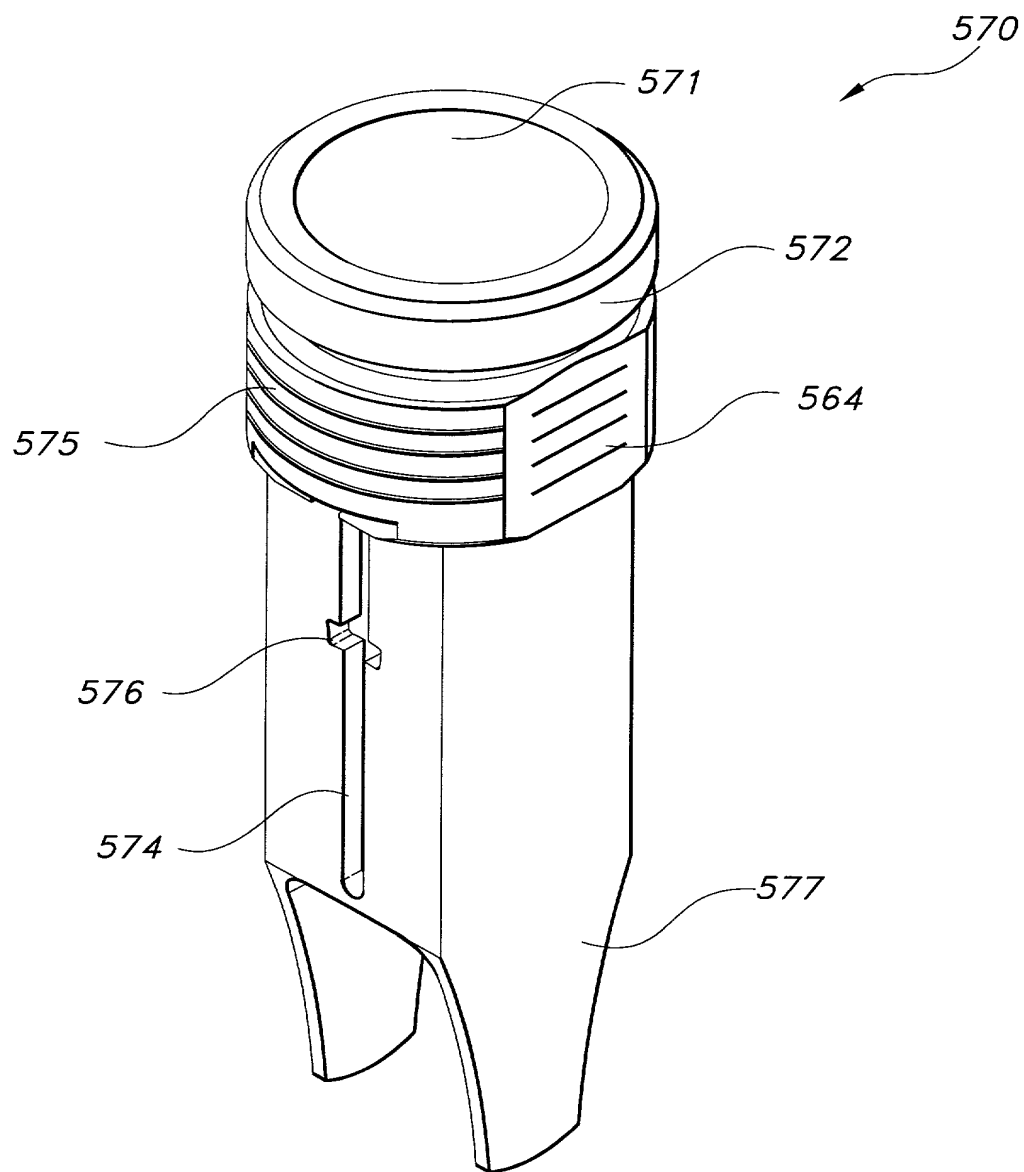


FIG 61

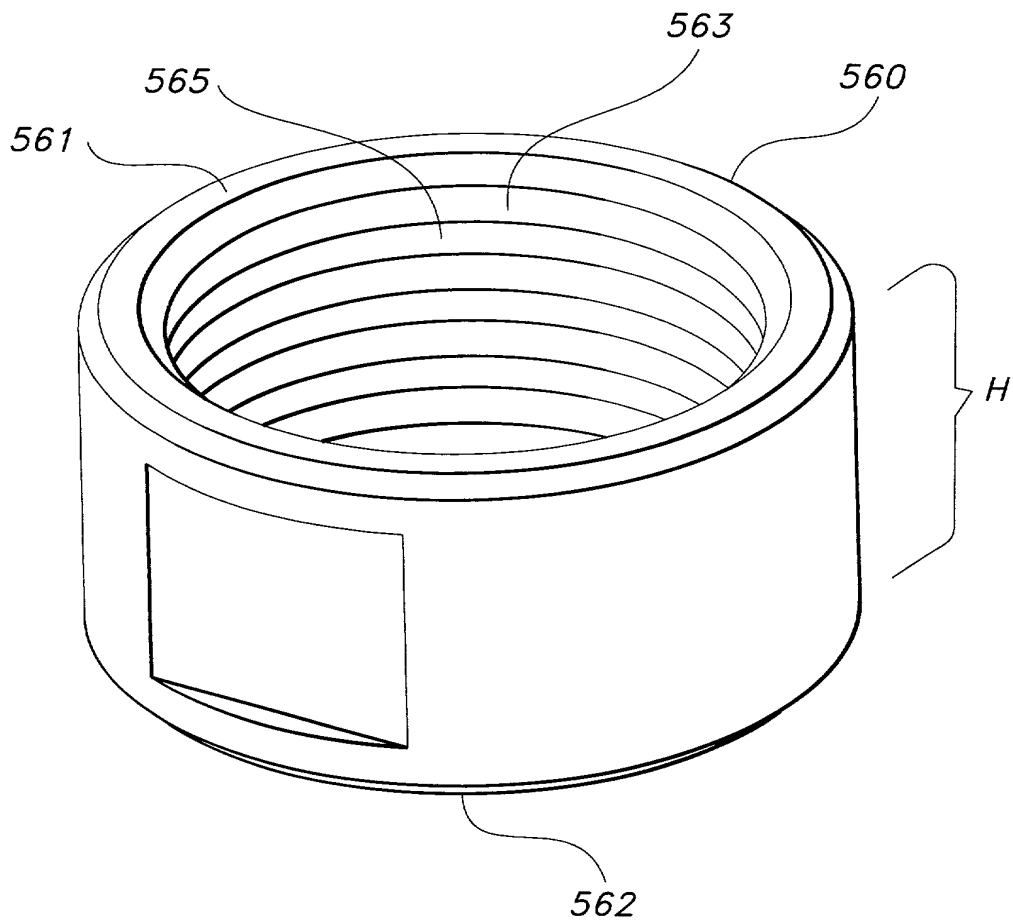


FIG 62

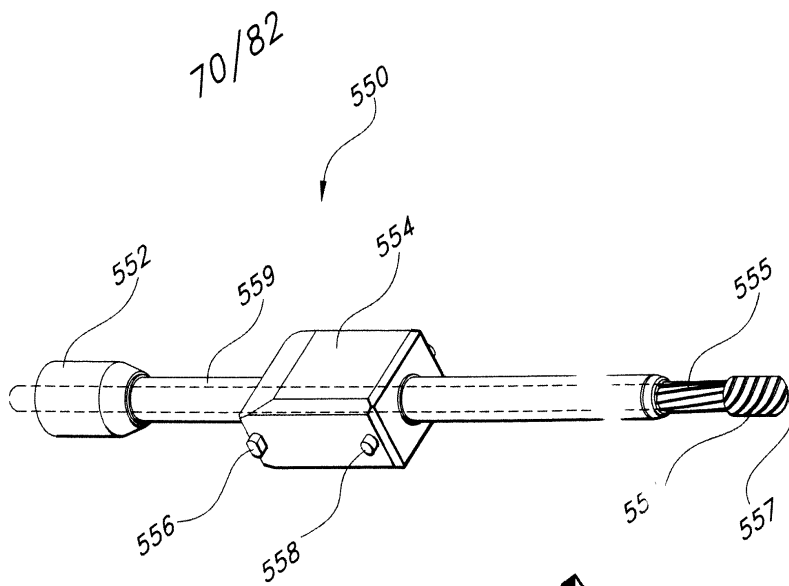


FIG 63A

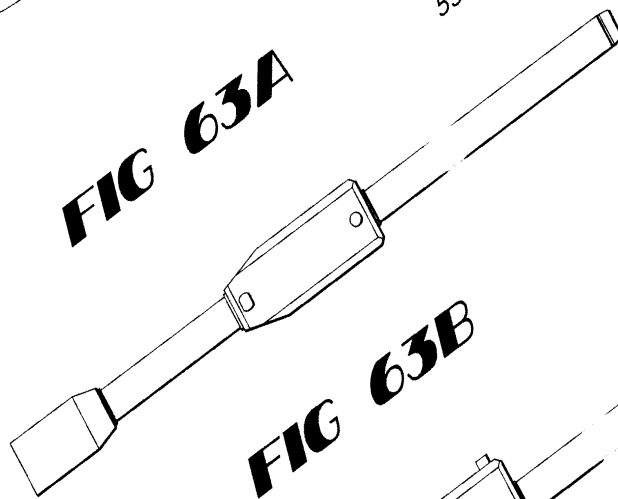


FIG 63B

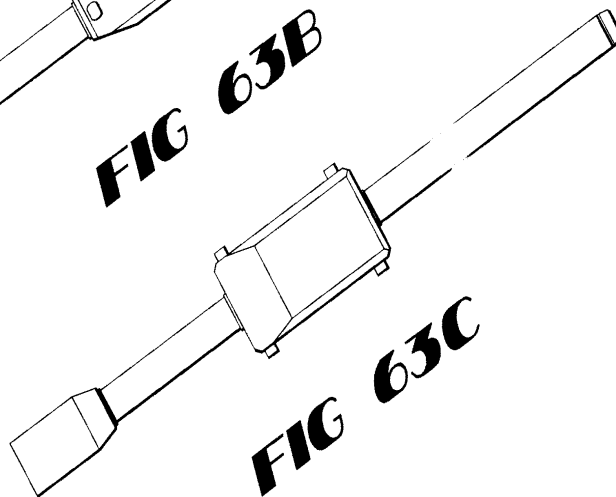


FIG 63C

Copyright © 2004 by Intel Corporation

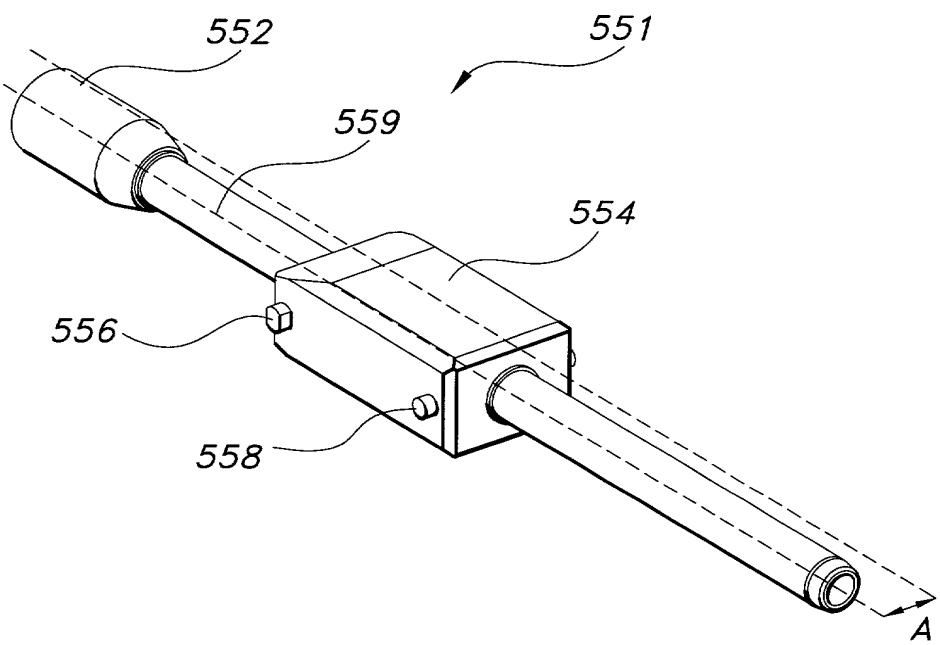


FIG 64A



FIG 64B

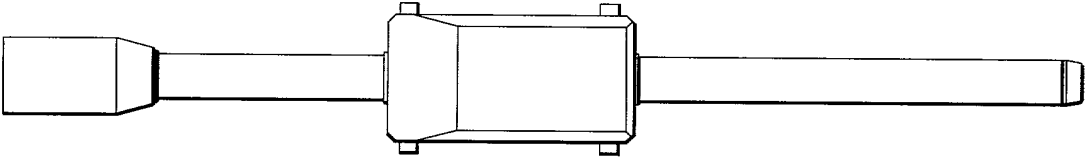


FIG 64C



Downloaded from www.elsevier.com



+

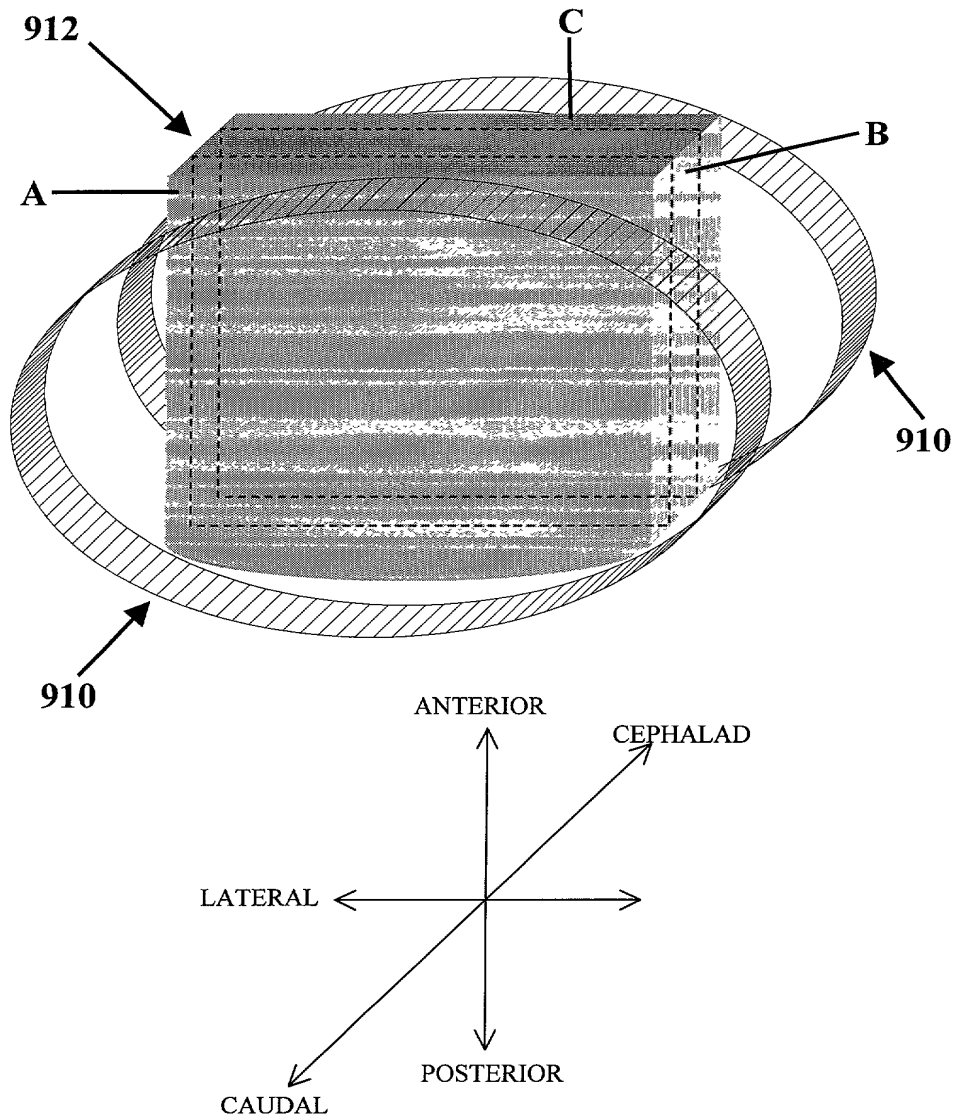


FIGURE 66

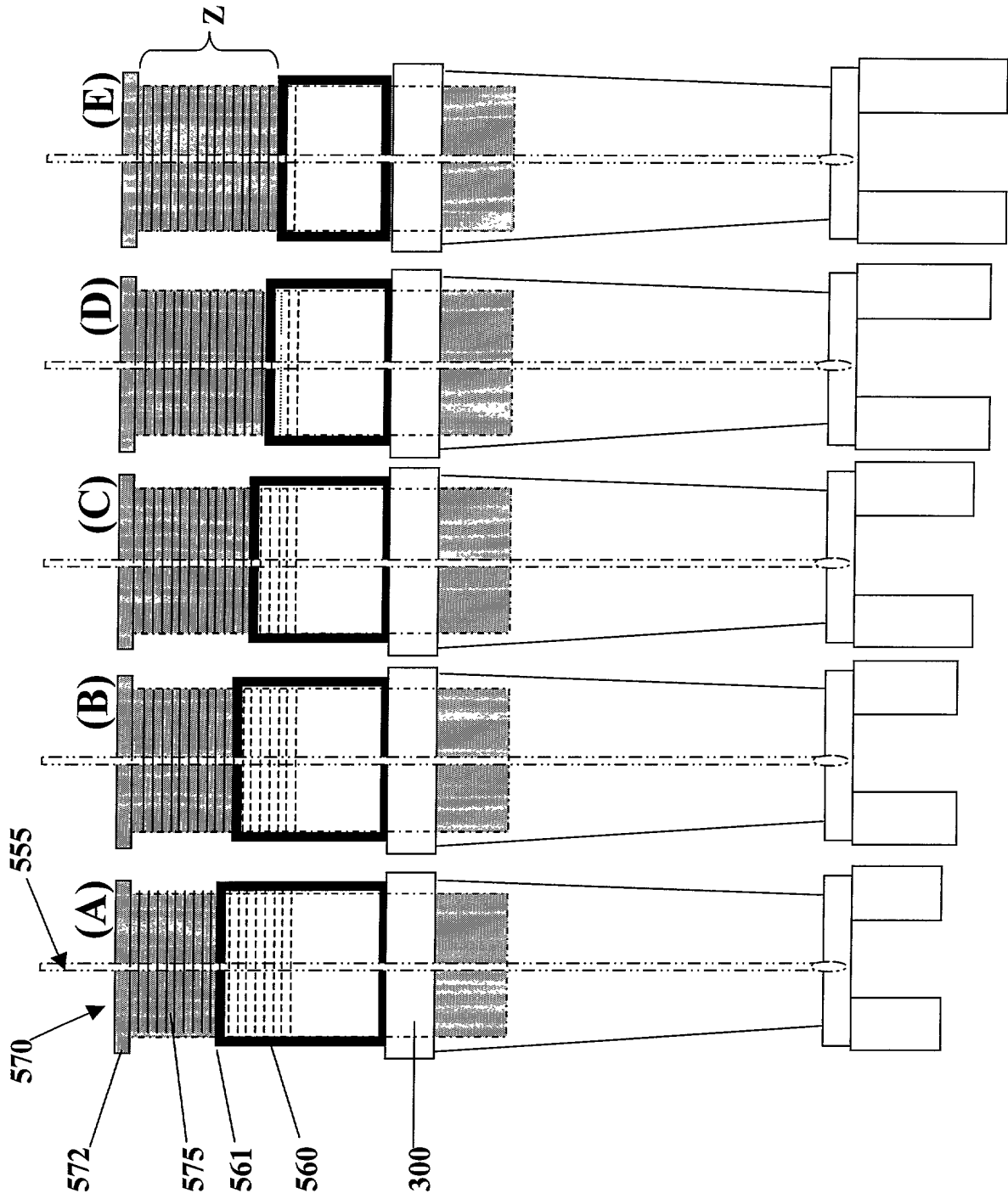


FIGURE 67

FIG. 68

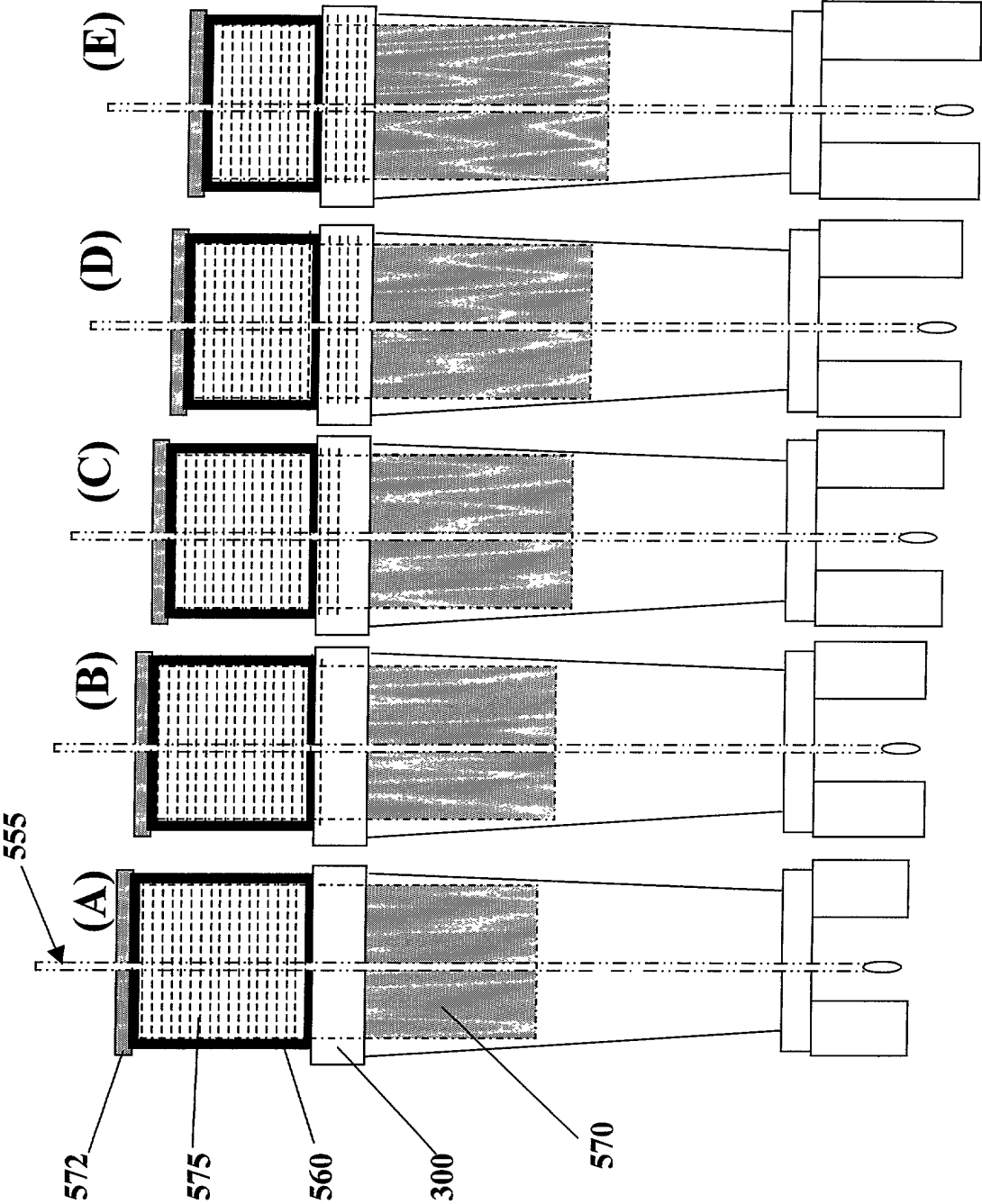


FIGURE 68

76/82

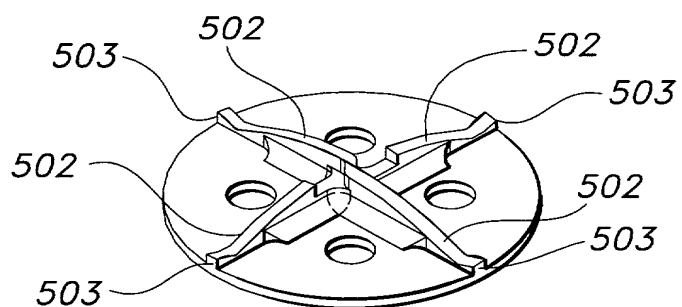
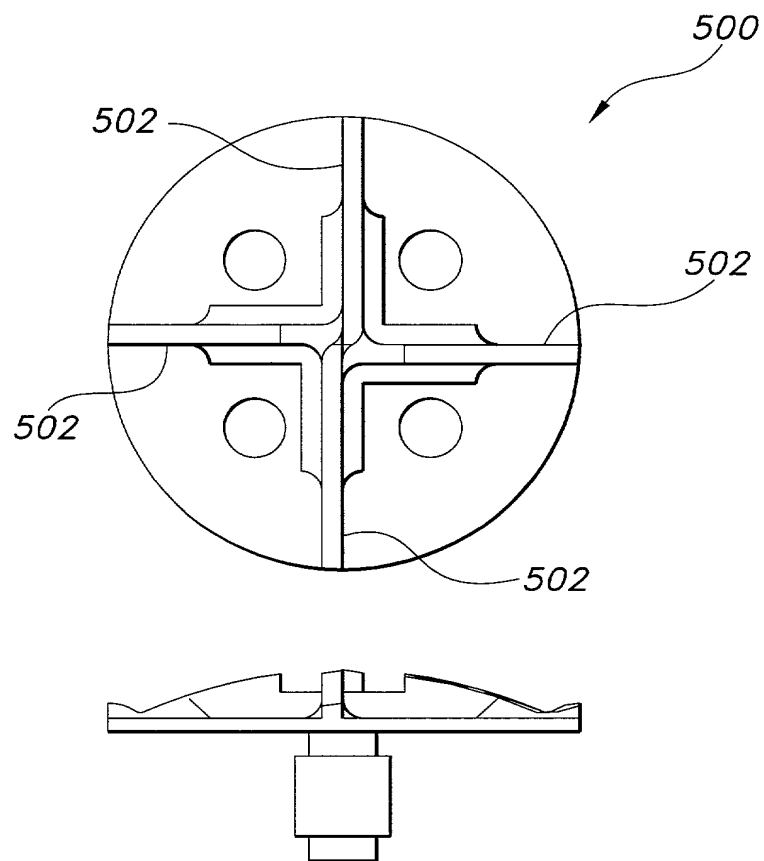
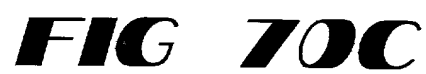


FIG 69



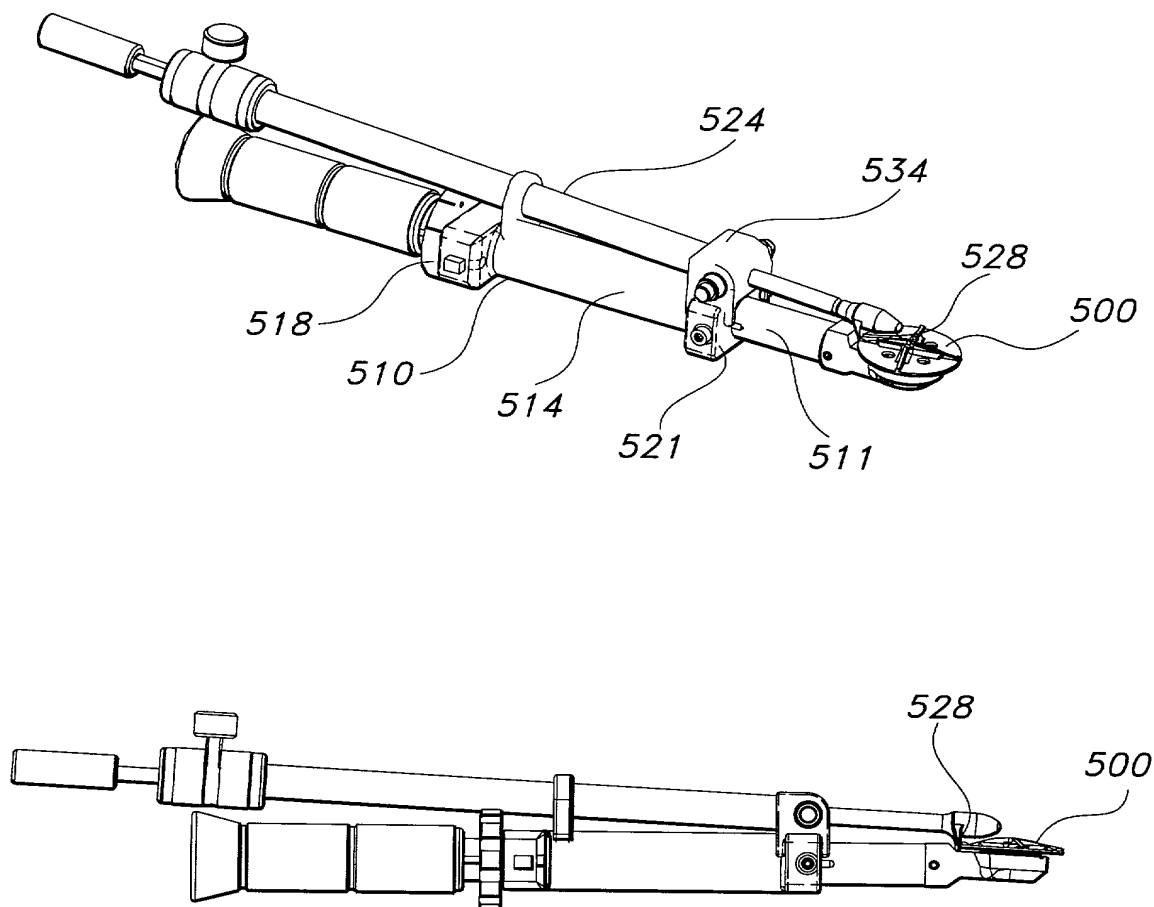


FIG 71

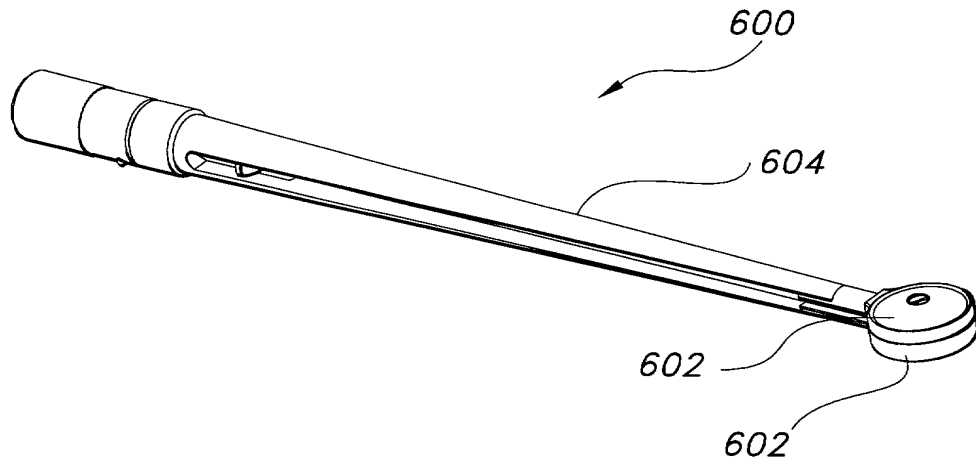


FIG 72 A

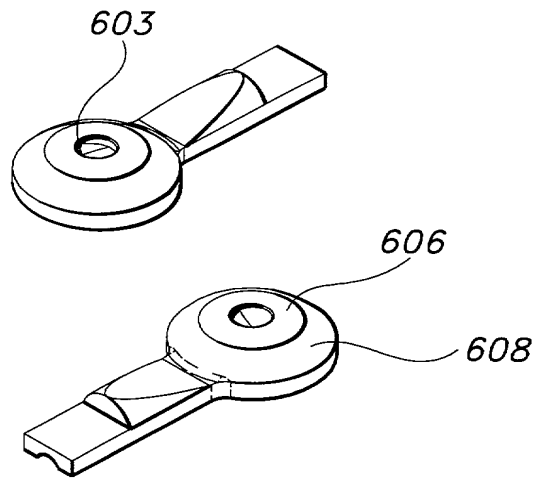


FIG 72 B



80/82

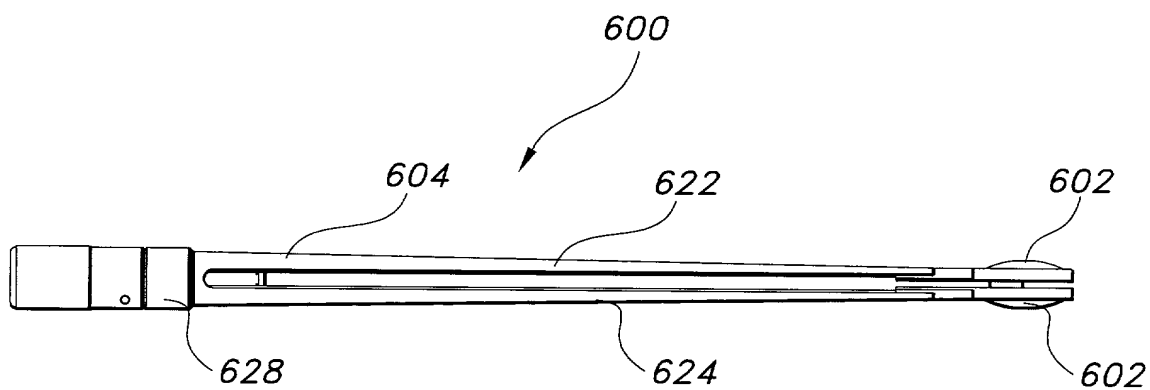


FIG 72C

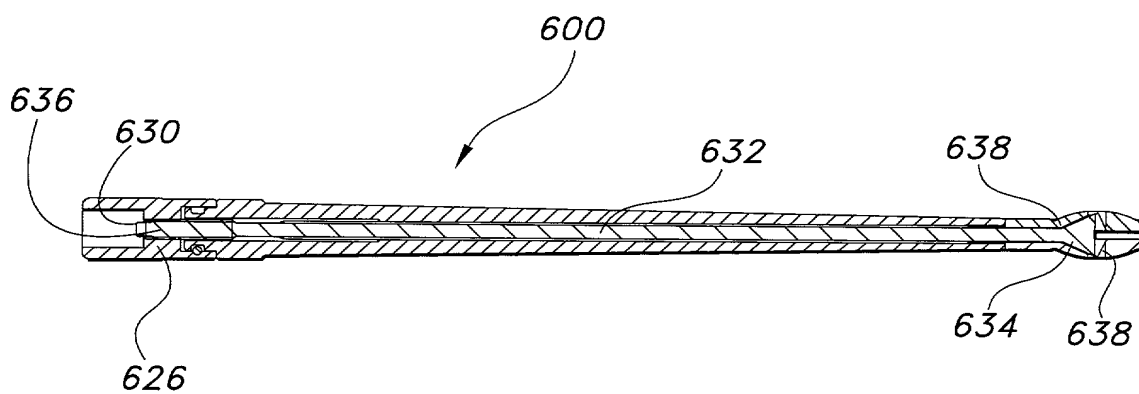


FIG 72D

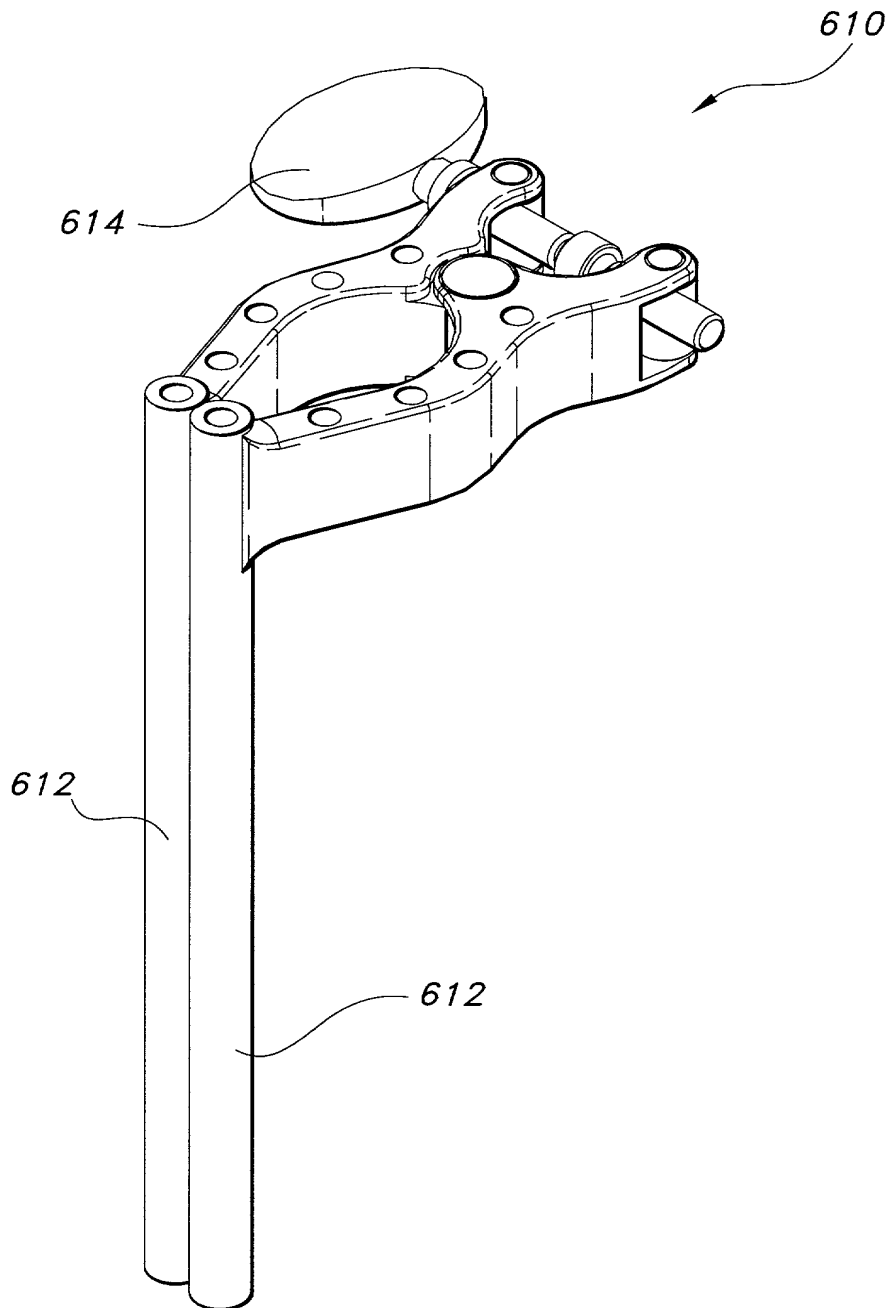


FIG 73



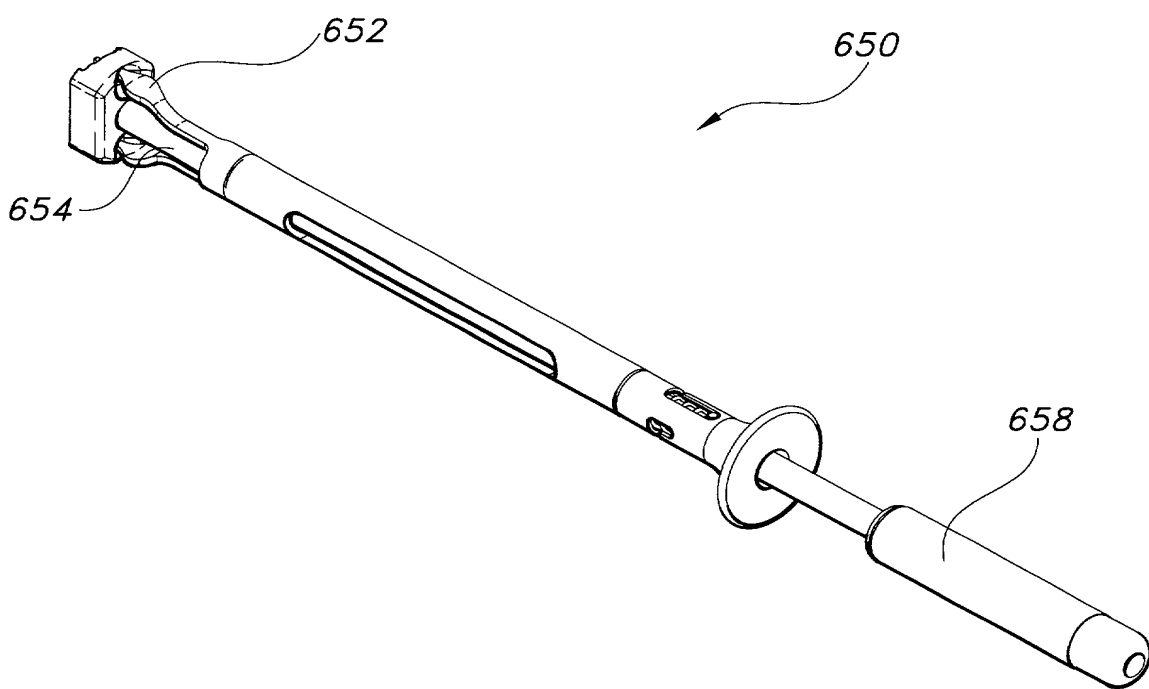


FIG 74